

NOTE S

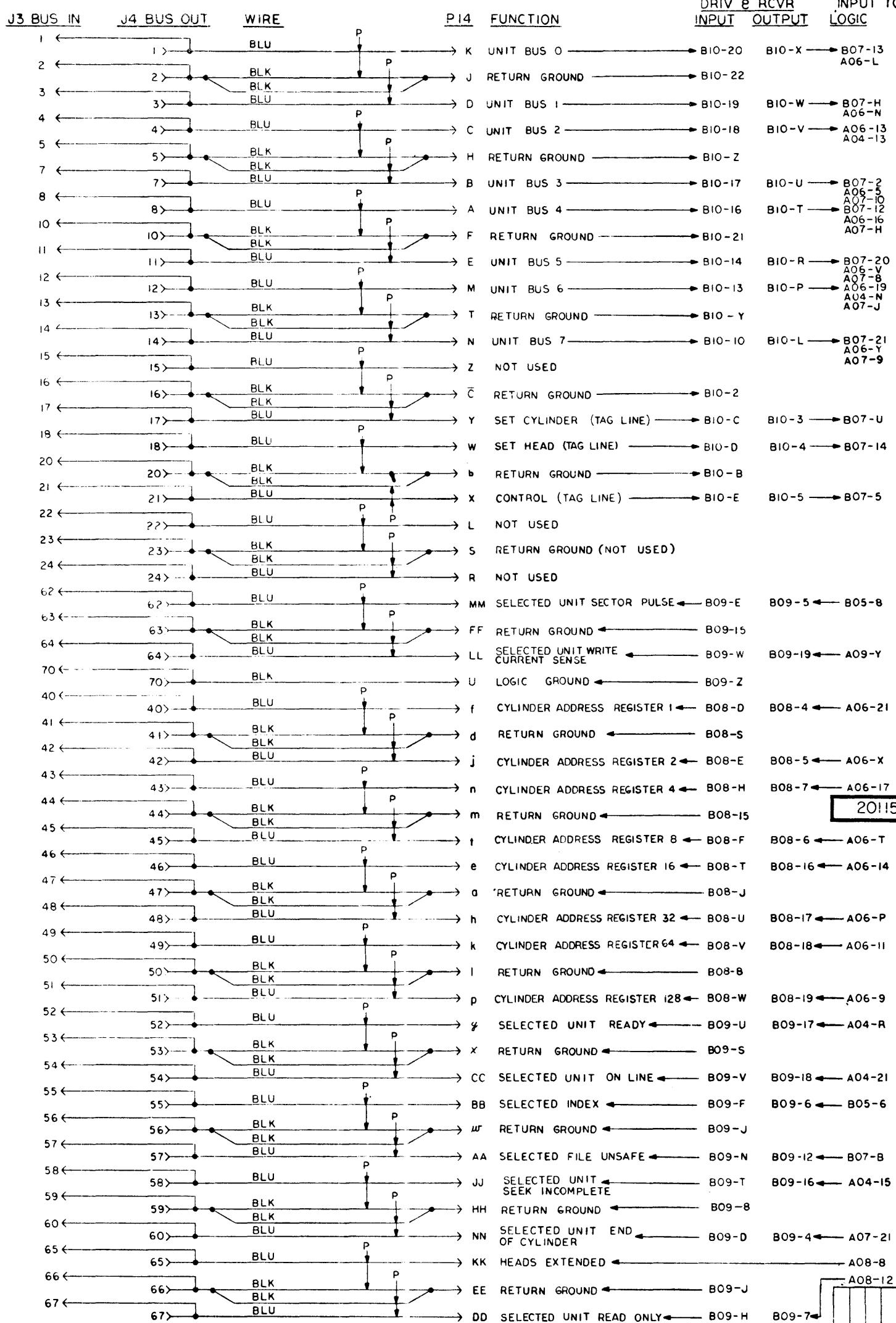
T1-SEEK INCOMPLETE DELAY 100 MIS
T2-FORWARD SEEK DELAY 1.5 MIS
T3-READY DELAY 7.5 MIS

FORWARD VELOCITY = .5 INCH/SEC OR GREATER

STROBE "R" 

RPOZ SEQ FLOW

201156

LOGIC PRINT REF
DRIV e RCVR INPUT TO
INPUT OUTPUT LOGIC


MATERIAL	NO
CASE DEPTH	2 PLACE DEC =

UNLESS OTHERWISE NOTED	PERIPHERAL SYSTEMS CORPORATION
ANGLES	NAME: Schematic Diagram

HARDNESS	BLUS IN LT
SURFACE TREATMENT	TYPE

CORNERS AND OR EDGES BROKEN	DETAIL
INSIDE MAX	SCALE

OTHERWISE NOTED	CHECK
	DRAW

APPROV	JUN 25 1968
	JUN 25 1968

201156	D
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002052

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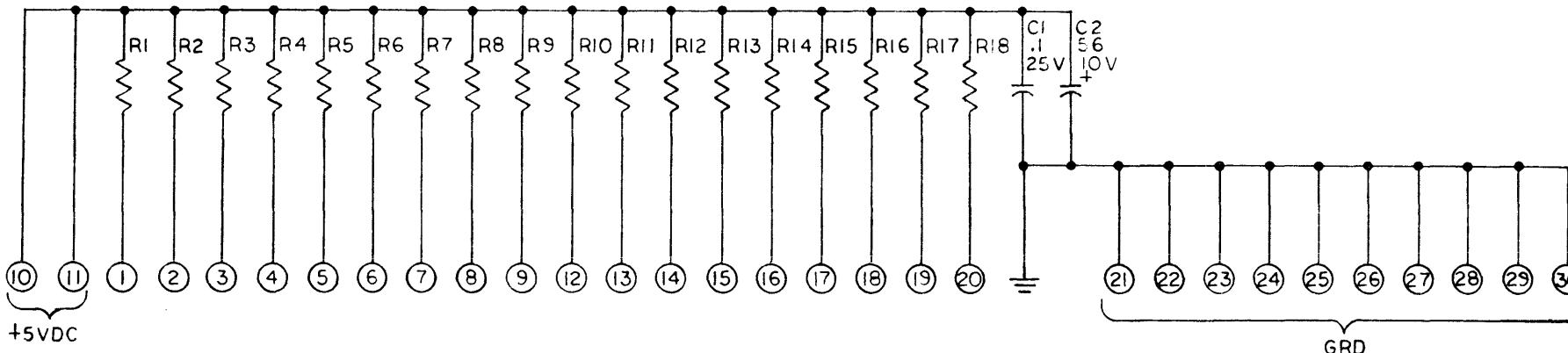
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002052

USED ON	RELEASED FOR ASSY	DATE	CHANGE NO	DATE	CHANGE NO
660	002051	5/1/69	305		
630-1		11-22-69	558		

002052

3



002052

2

NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE 56 OHMS, 1W, $\pm 5\%$
2. ALL CAPACITORS ARE IN MICROFARADS

1000-10000

002052

2

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MATERIAL	NO	TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ± 3 PLACE DEC ± ANGLES ±	Peripheral Systems Corporation a subsidiary of MEMOREX CORPORATION		
CASE DEPTH				NAME SCHEMATIC DIAGRAM		
HARDNESS				LINE TERMINATOR IV		
SURFACE TREATMENT				DESIGN		TYPE
002052		C	CORNERS AND OR EDGES BROKEN	OUTSIDE MAX	DETAIL	SCALE
			RADIUS UNLESS OTHERWISE NOTED	INSIDE MAX	CHECK	DRAW
					APPRO	BRH 4-25-69

201157

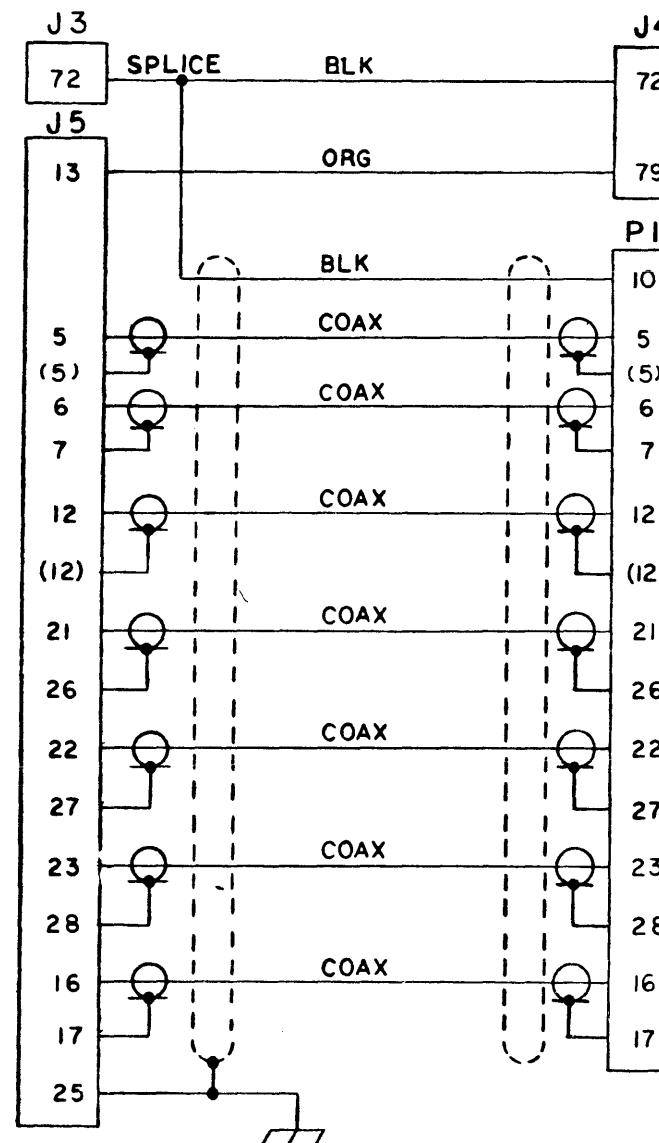
1

201157

USED Q.N	RELEASED FOR ASSY	DATE	CHANGE NO	DATE	CHANGE NO
660-1	201062	6/6/69	364		
		1/21/69	537		
		APR 9, 70	865		

201157

1



LOGIC PRINT REF

DRIV & RCVR INPUT TO
INPUT OUTPUT LOGIC

GROUP

WRITE DATA
GROUND —
SPARE

READ DATA →

GROUND →

AIO-4

AIO-C

ATTENTION ← B08-L B08-10 ← A04-17

COMMON ← B08-

UNIT IS SELECTED ← B08-K B08-9 ← B07-

COMMON ← B08

SELECT UNIT → BIO-9 → BIO-K → B07-

COMMON → **BIO-**

SECTOR PULSE

COMMON B09-1

B

5

2

10

NOTES.

- I. ALL OTHER PINS IN J5 ARE NOT USED INCLUDING PIN G.

MATERIAL		NO	TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ±	Peripheral Systems Corporation subsidiary of MEMOREX CORPORATION			
CASE DEPTH				3 PLACE DEC ±	NAME		SCHEMATIC DIAGRAM	
HARDNESS				ANGLES ±	UNIT INPUT			
SURFACE TREATMENT				OUTSIDE MAX	DESIGN		TYPE	
201157		C	CORNERS AND / OR EDGES BROKEN	INSIDE MAX	DETAIL		SCALE	
RADIUS UNLESS OTHERWISE NOTED				CHECK		DRAW	JLW 2-4-69	
				APPRO	1/15/69 1/15/69	CHECK	Janice 6/6/69	

2011E7

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20112

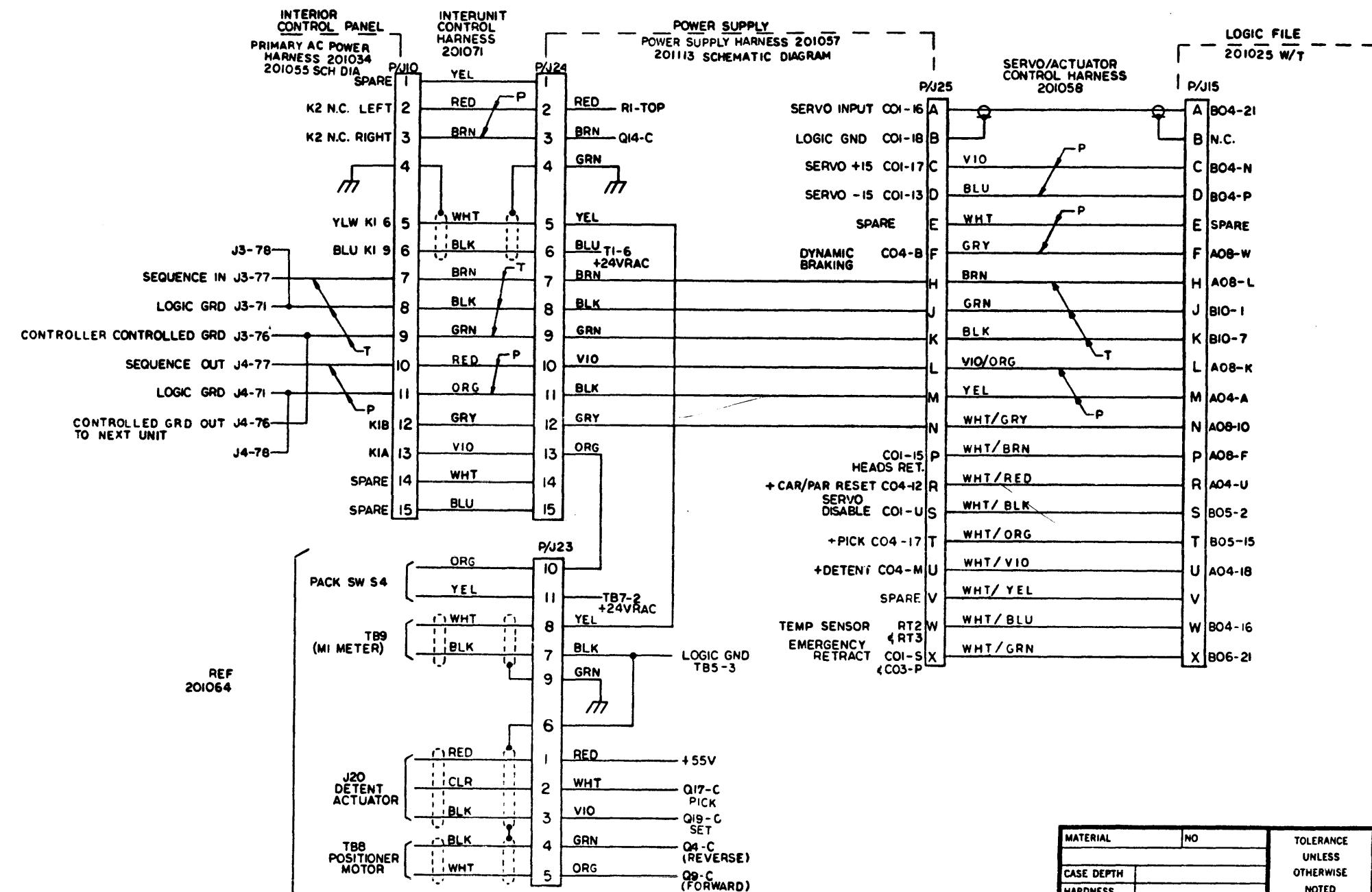
201112

USED ON	RELEASED FOR ASSY	DATE	CHANGE NO	DATE	CHANGE NO
660	MANUAL	6/6/69	364		
		11/9/70	865		
		OCT 70	1525		

2011/2

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1



MATERIAL	NO	TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ± 3 PLACE DEC ± ANGLES ±	Peripheral Systems Corporation a subsidiary of MEMOPAC CORPORATION		
CASE DEPTH		CORNERS AND/OR EDGES BROKEN	OUTSIDE INSIDE	NAME	SCHEMATIC DIAGRAM INTERUNIT CONTROL	
HARDNESS				DESIGN	E-1112	TYPE
SURFACE TREATMENT		DETAIL		SCALE	NONE	
201112 D		CHECK	E-1112	DRAW	KEN BLEND 1-10-69	
		APPRO	E-1112	CHECK	REC-LL6/6/69	
		RADIUS UNLESS OTHERWISE NOTED				

201112

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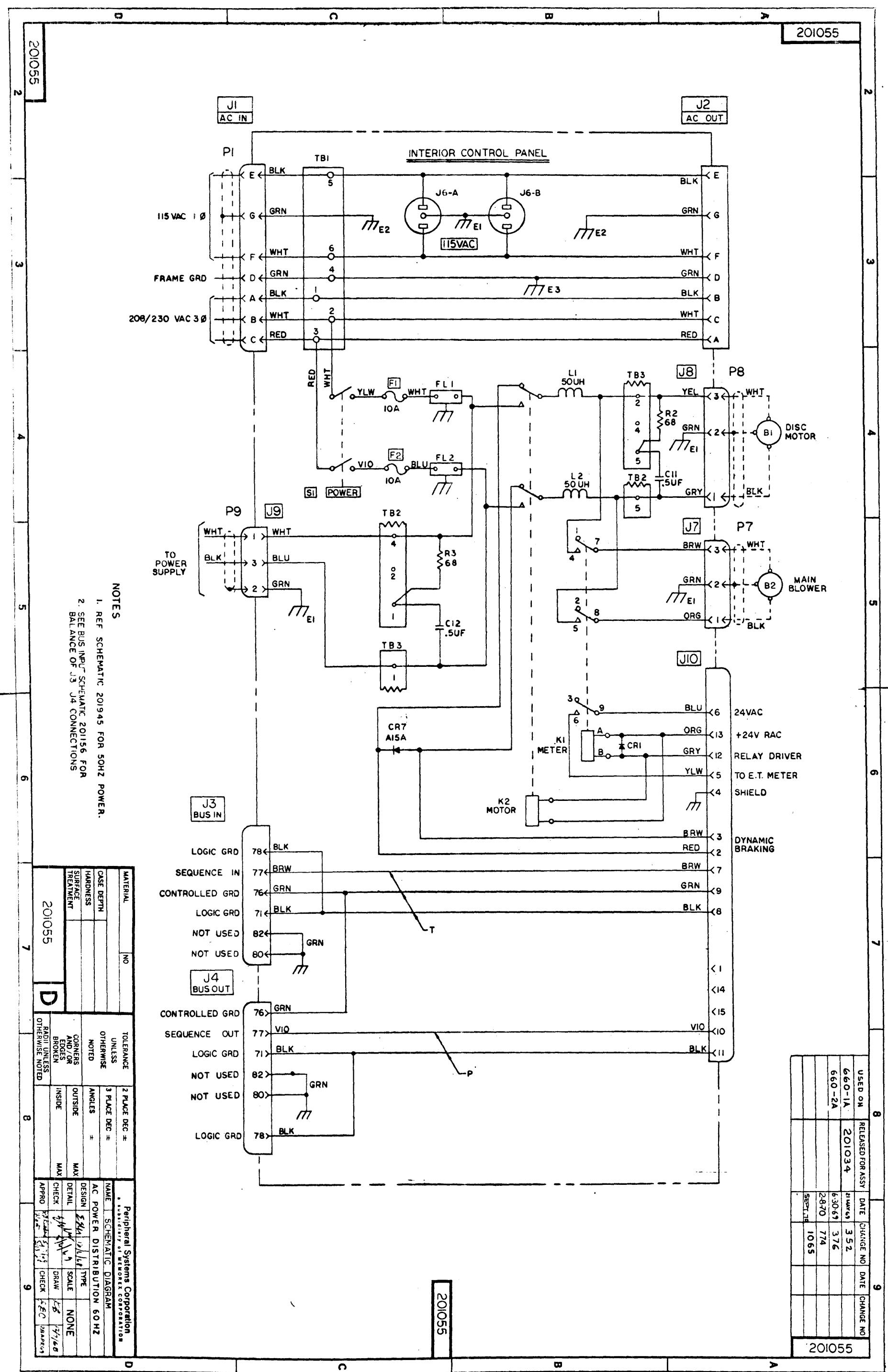
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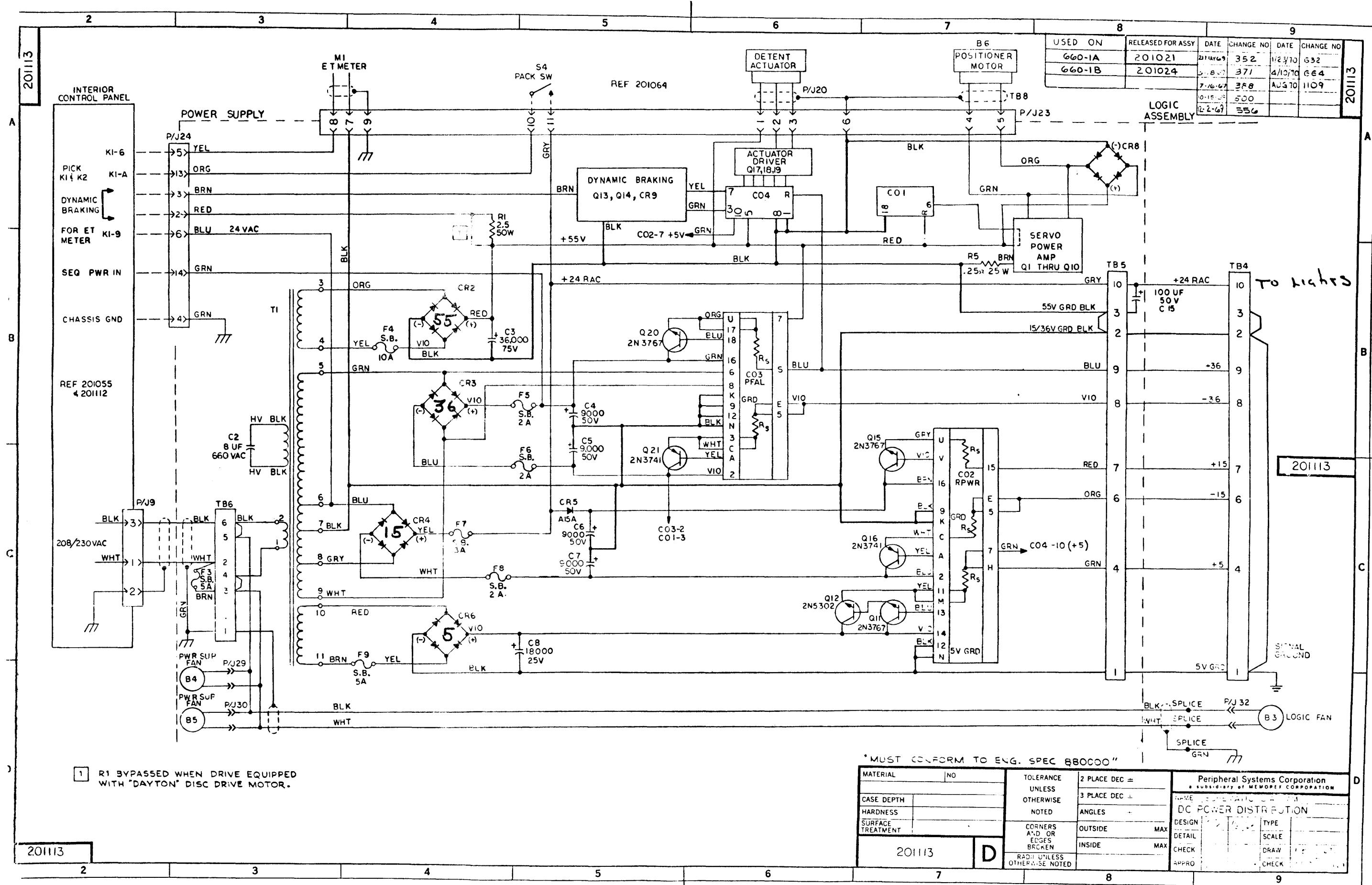
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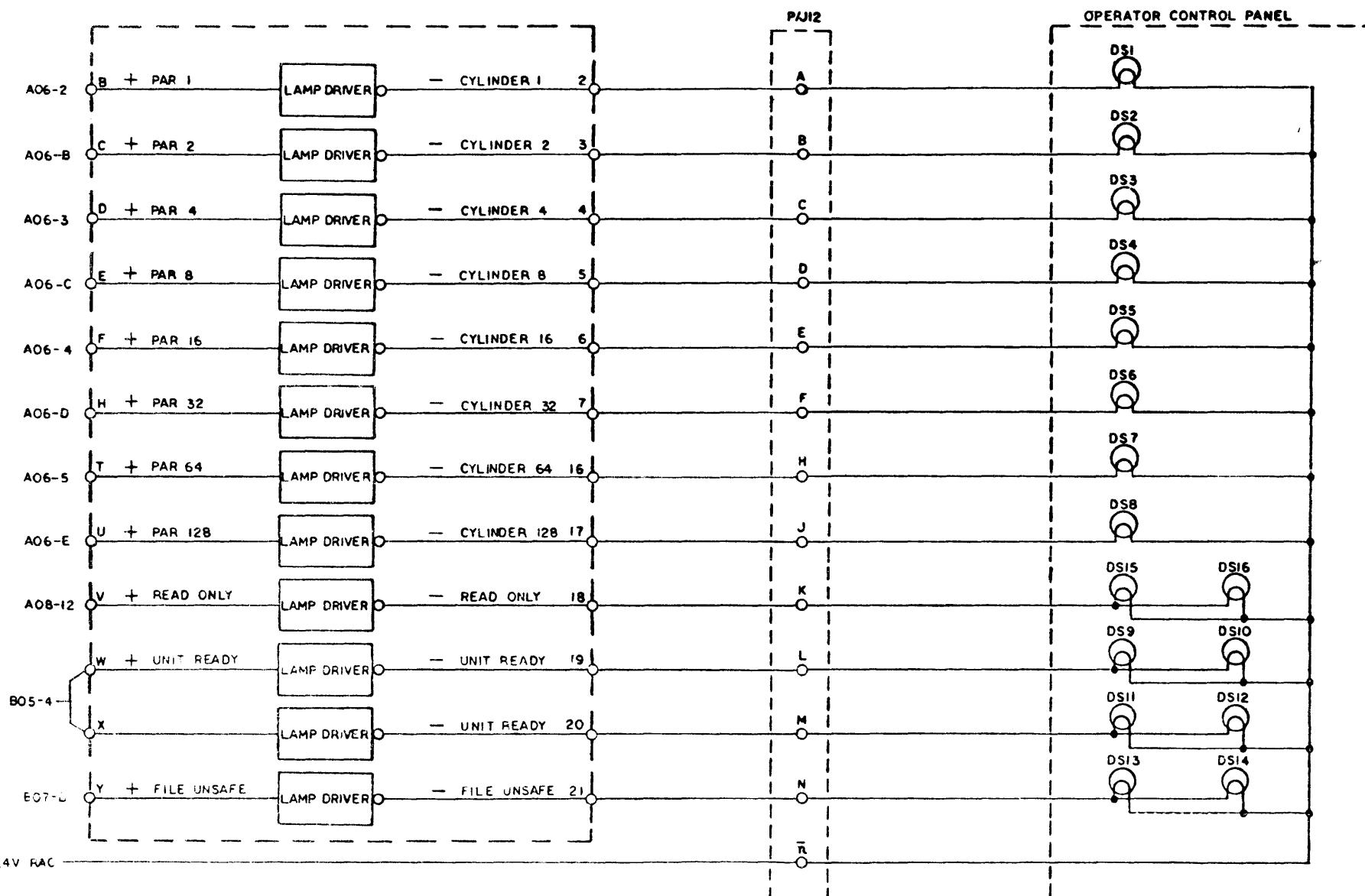
8 7 6 5 4 3 2 1

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201327

RELEASED FOR ASSEMBLY
MANUAL

D



NOTES: UNLESS OTHERWISE SPECIFIED

- ON MACHINES WITH TAMP P.C. BOARD OF EC LEVEL 487 OR GREATER, THE MAGNITUDE OF THE LOGIC SUPPLY VOLTAGE (+5V) IS 5.10 VOLTS ± .04 VOLTS.

A02
(LDRV)

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201327

DATE	CHANGE NO.	DATE	CHANGE NO.	TECHNICAL APPROVAL	MEMOREX EQUIPMENT GROUP
7-29-69	416				NAME SYSTEM LOGIC DIAGRAM
12-2-69	556				LAMP DRIVERS 660-1
APR 9, 70	865				DESIGN
AUG 10, 70	1158				DETAIL
					CHECK
					APPRO

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201327

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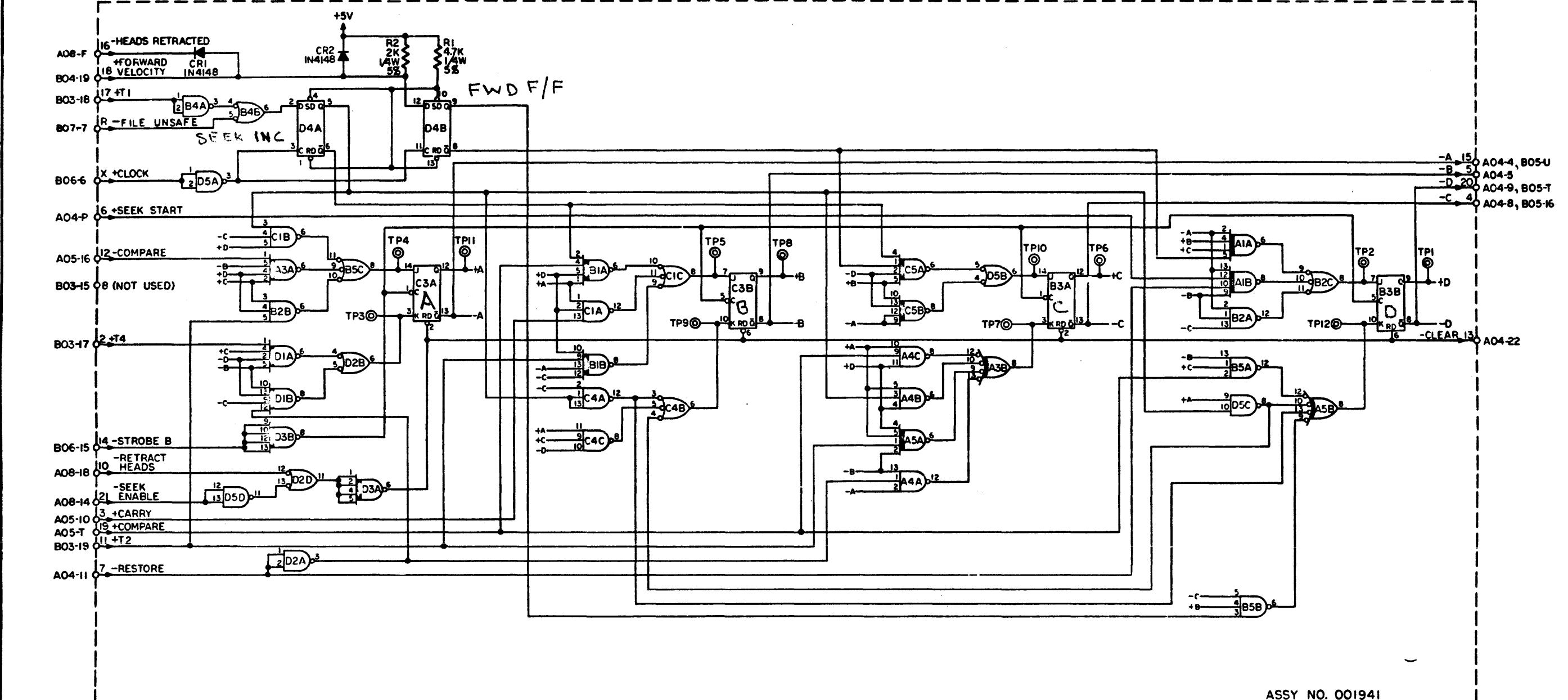
9

201142

201142

USED ON	RELEASED FOR ASSY	DATE	CHANGE NO	DATE	CHANGE NO
660	MANUAL	4-29-69	319H		
		8-2-69	417		
		7-25-69	429		
		12-11-69	429C		
		1/1/71	846		

201142



NOTES:

1. IC'S B3 & C3 ARE SN7473N, DUAL J-K FLIP-FLOP.
2. IC D4 IS A SN7474N, DUAL D-TYPE FLIP-FLOP.
3. FOR MACHINES WITH TAMP P.C. BOARD AT EC LEVEL 487 OR GREATER, THE MAGNITUDE OF THE LOGIC SUPPLY VOLTAGE (+5V) IS + 5.10 V

201142

MATERIAL NO		TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ±		Peripheral Systems Corporation a subsidiary of MEMOREX CORPORATION	
CASE DEPTH	HARDNESS		3 PLACE DEC ±		NAME SYSTEM LOGIC DIAGRAM 660-1 SERVO SEQUENCER	
SURFACE TREATMENT		NOTED	ANGLES	±	DESIGN	TYPE
			CORNERS AND/OR EDGES BROKEN	OUTSIDE MAX	DETAIL	SCALE NONE
			INSIDE MAX	INSIDE MAX	CHECK	DRAW
			RADIUS UNLESS OTHERWISE NOTED		APPRO	CHECK DRW

201142

D

A03
(SSEQ)

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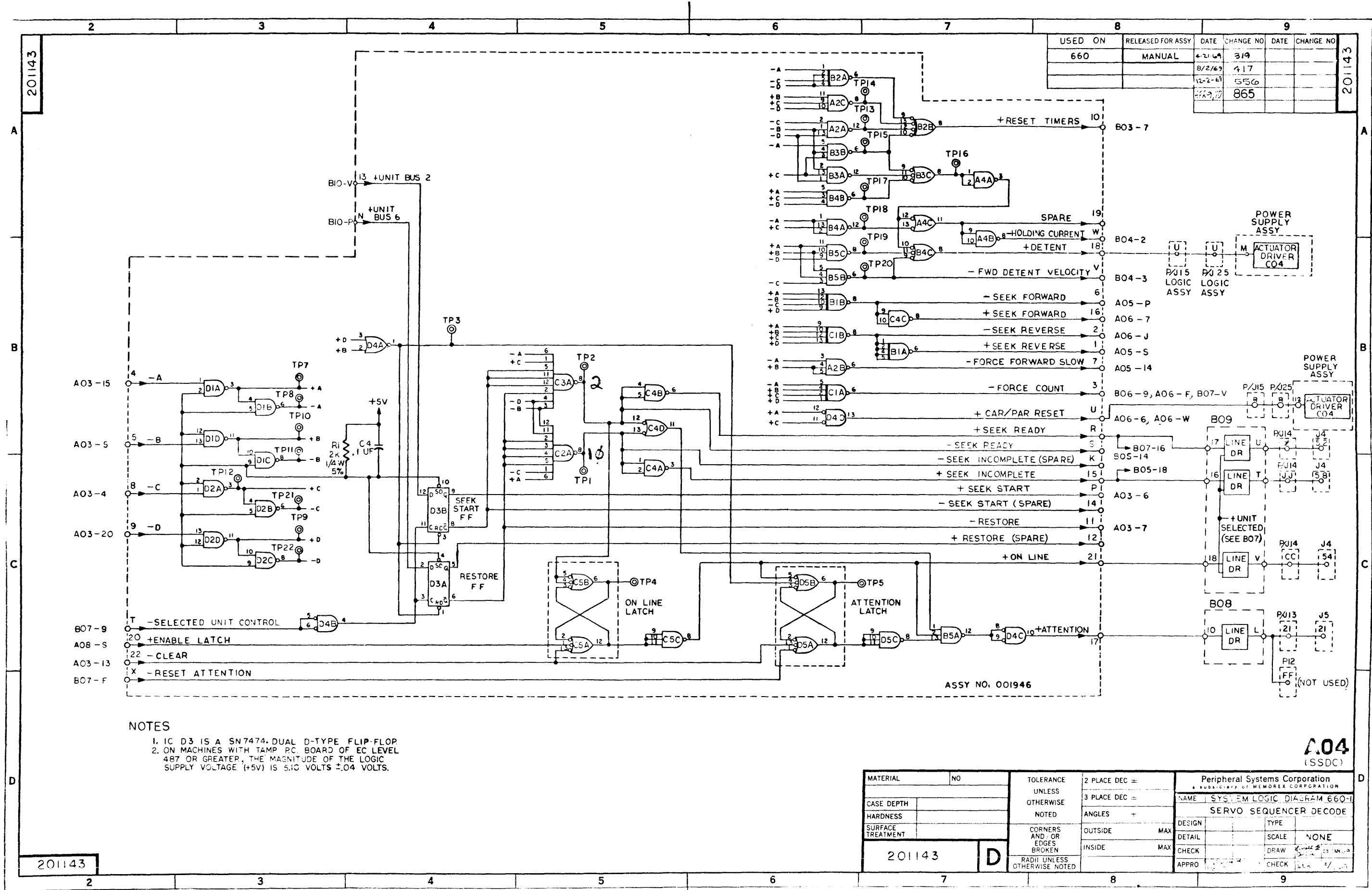
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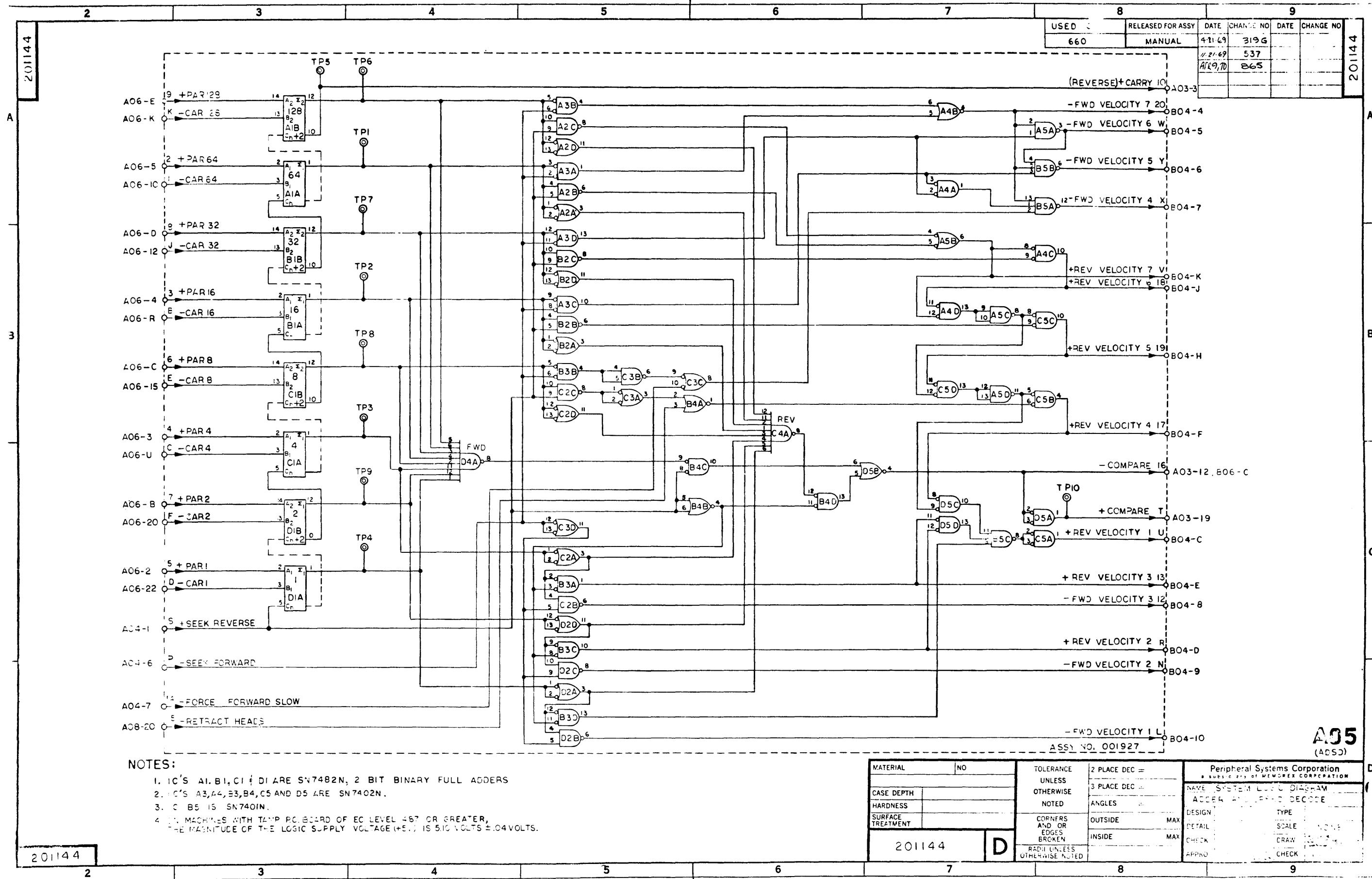


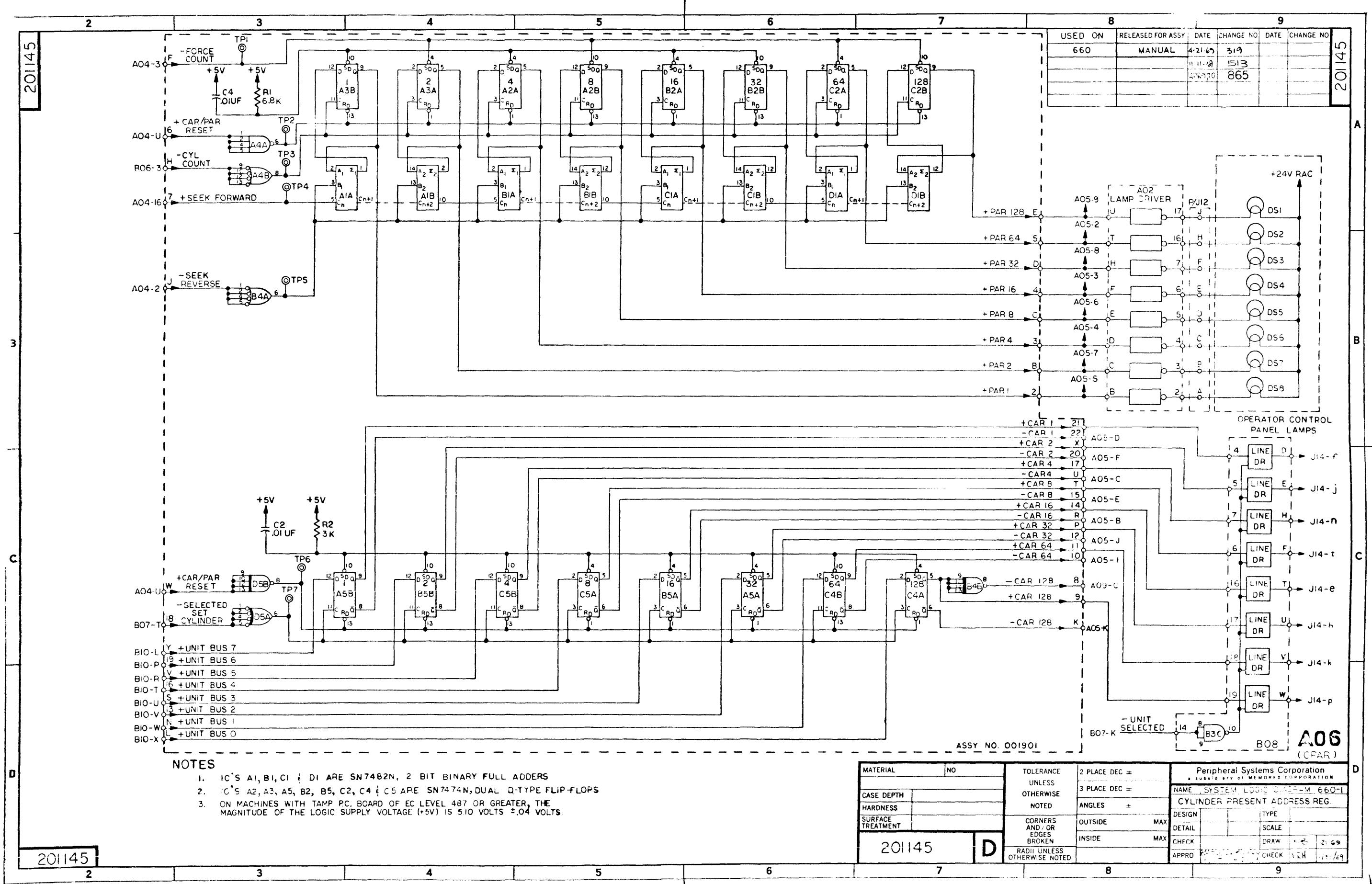
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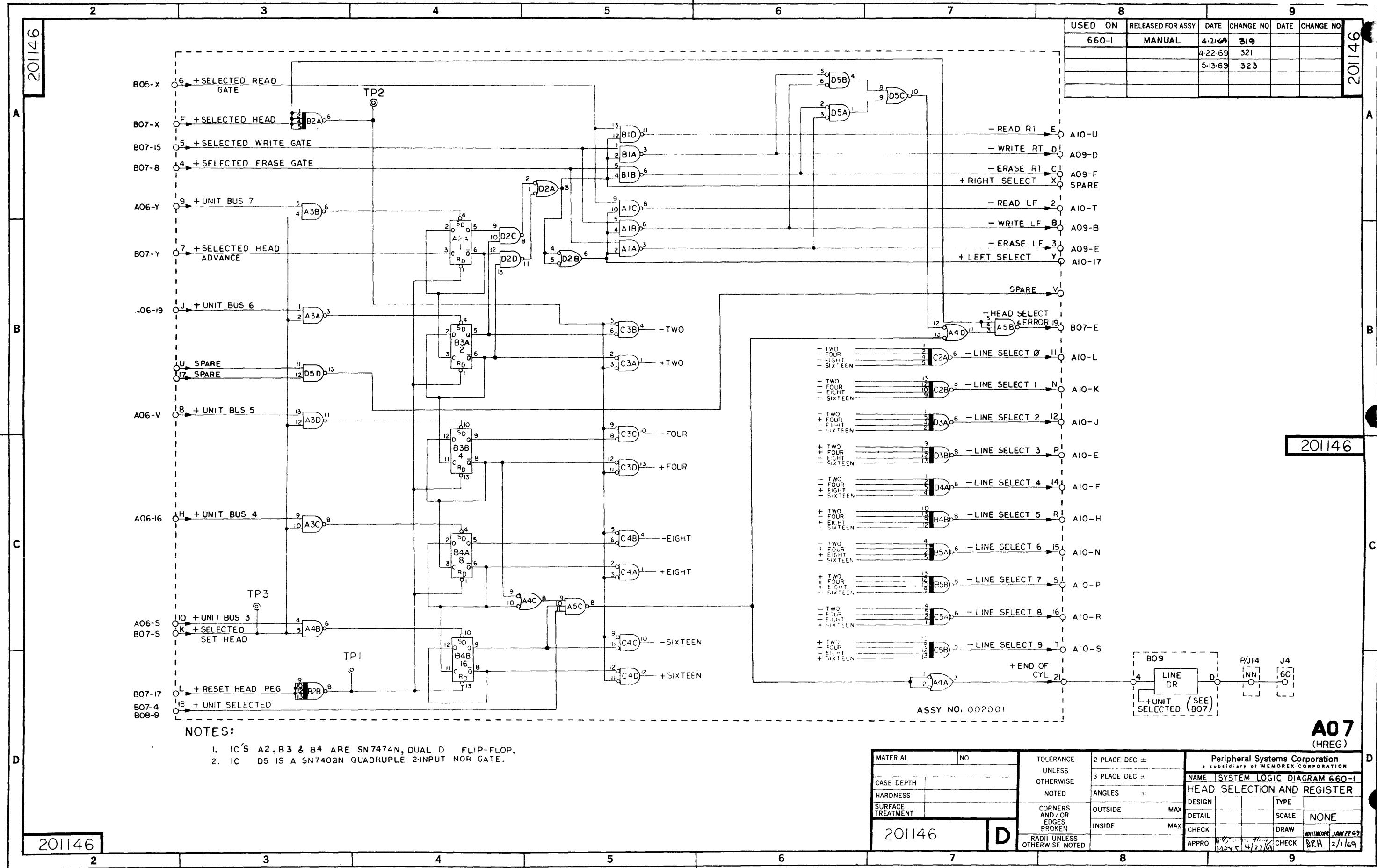
1. IC D3 IS A SN7474, DUAL D-TYPE FLIP-FLOP.
 2. ON MACHINES WITH TAMP. P.C. BOARD OF EC LEVEL 487 OR GREATER, THE MAGNITUDE OF THE LOGIC SUPPLY VOLTAGE (+5V) IS 5.10 VOLTS $\pm .04$ VOLTS.

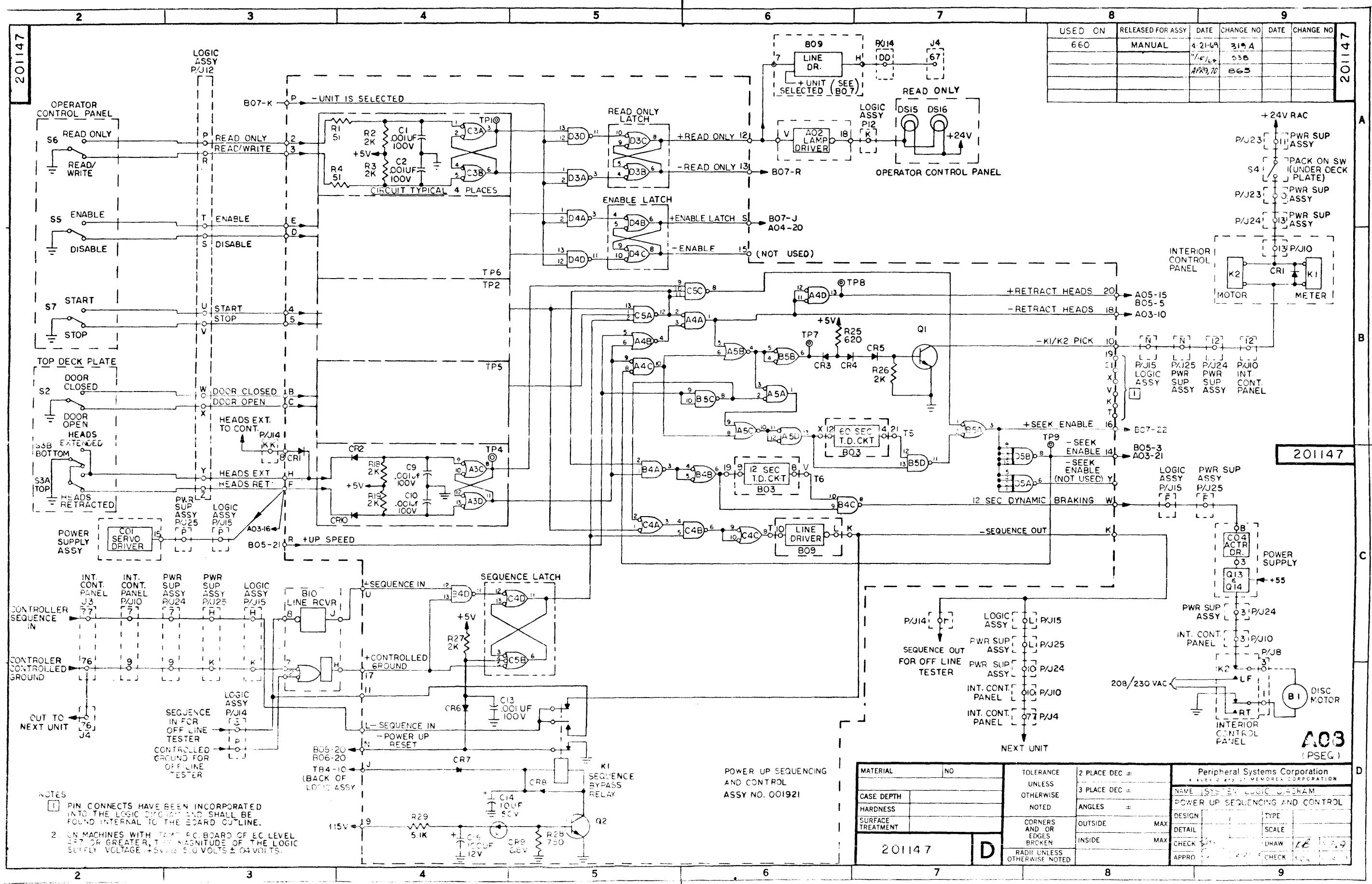
A.04
(SSDC)

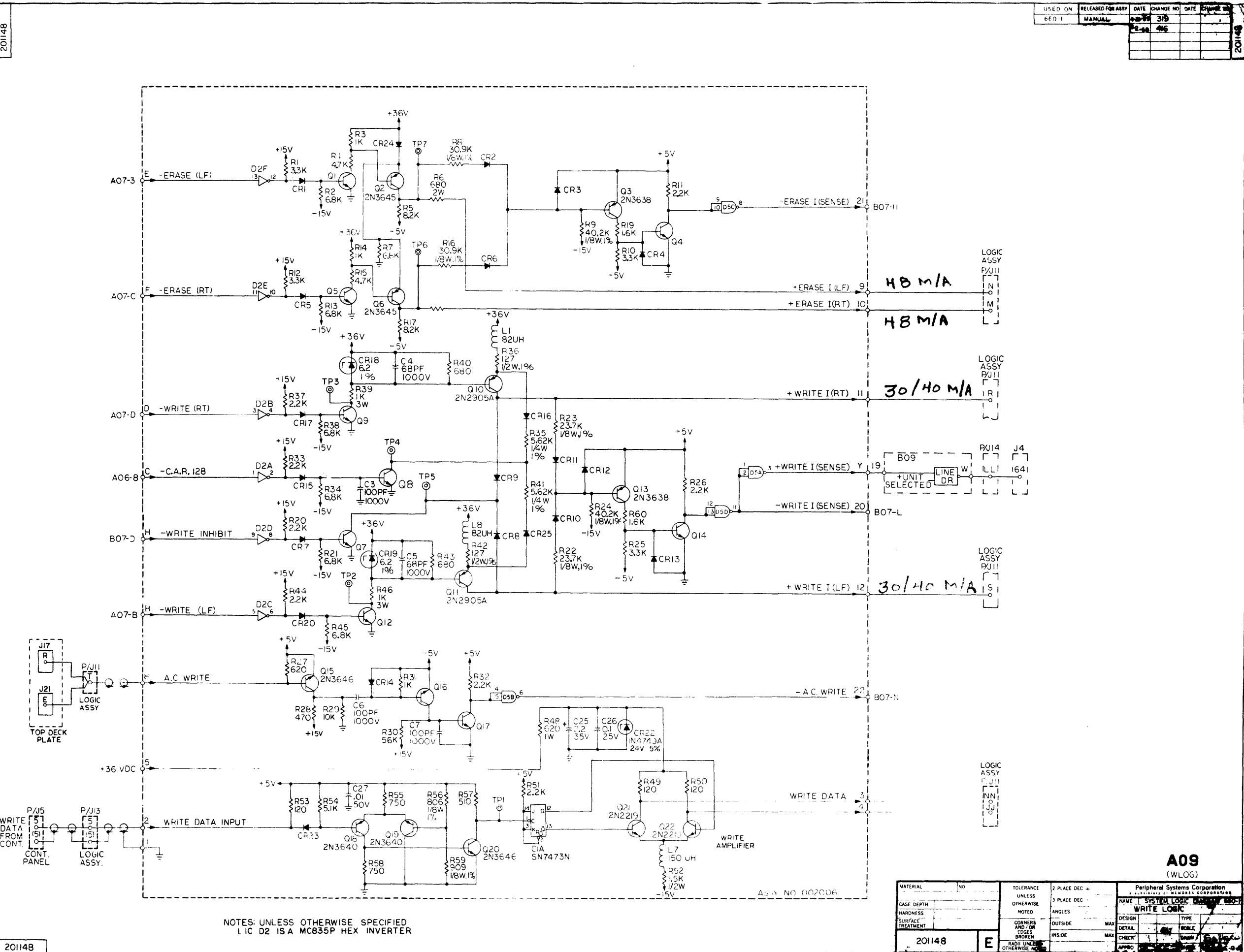
MATERIAL		NO	TOLERANCE	2 PLACE DEC ±		Peripheral Systems Corporation A SUBSIDIARY OF MEMOREX CORPORATION		
			UNLESS	3 PLACE DEC ±		NAME SYSTEM LOGIC DIAGRAM 660-1		
CASE DEPTH			OTHERWISE			SERVO SEQUENCER DECODE		
HARDNESS			NOTED	ANGLES	+			
SURFACE TREATMENT			CORNERS AND / OR EDGES BROKEN	OUTSIDE	MAX	DESIGN	TYPE	
		D	RADIU UNLESS OTHERWISE NOTED	INSIDE	MAX	DETAIL	SCALE	NONE
201143						CHECK	DRAW	KW-22 JAN. 29
						APPRO	CHECK	2/29

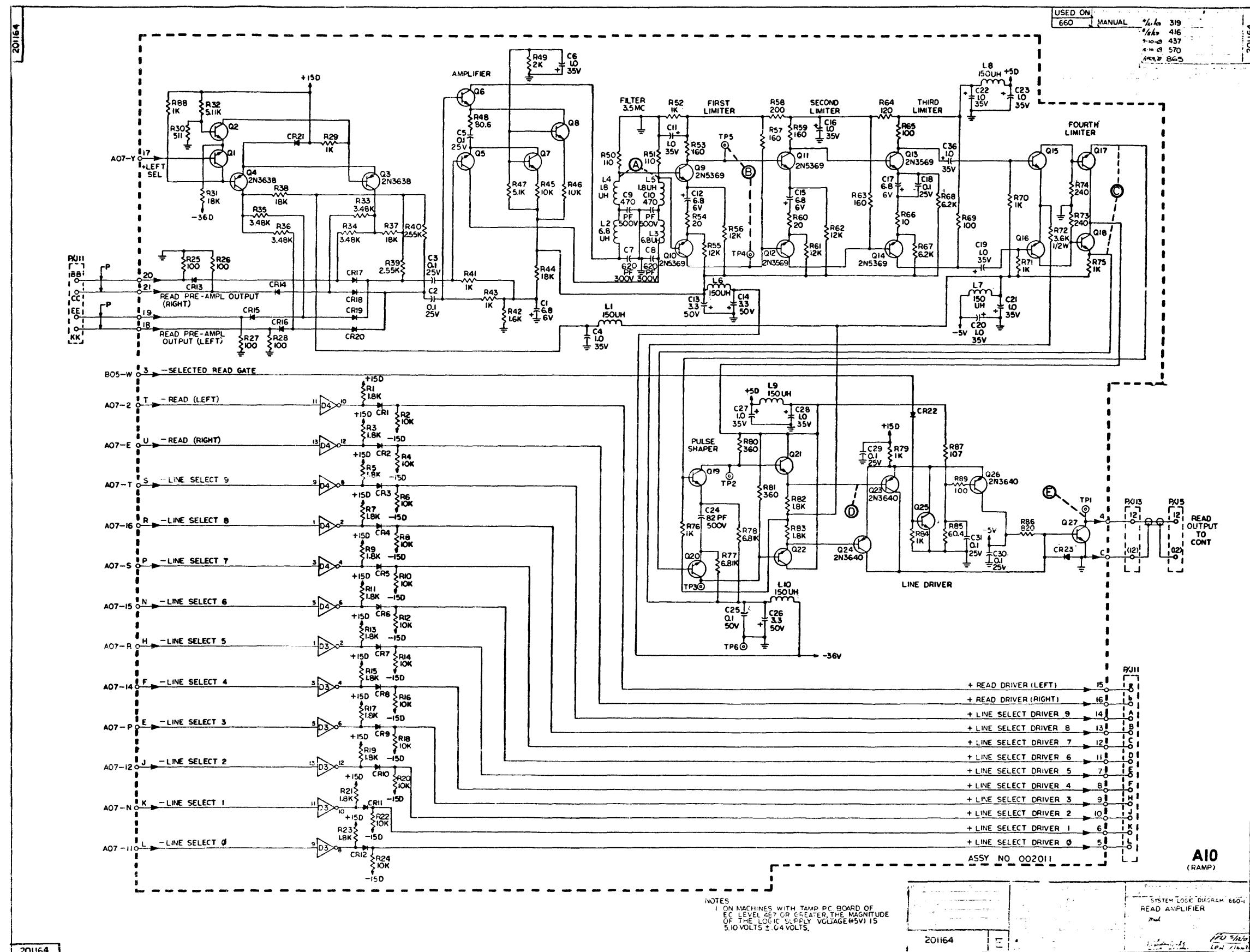


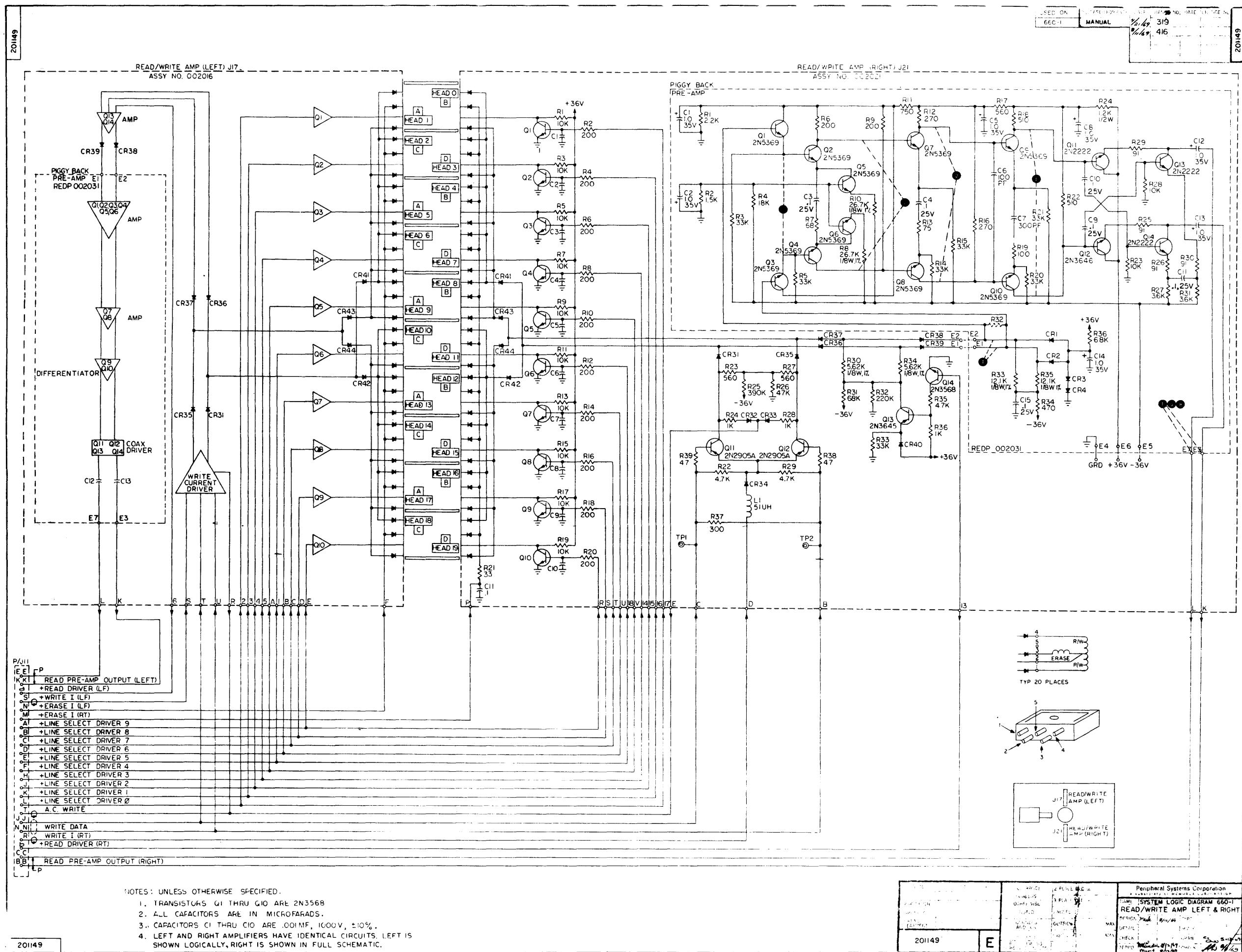


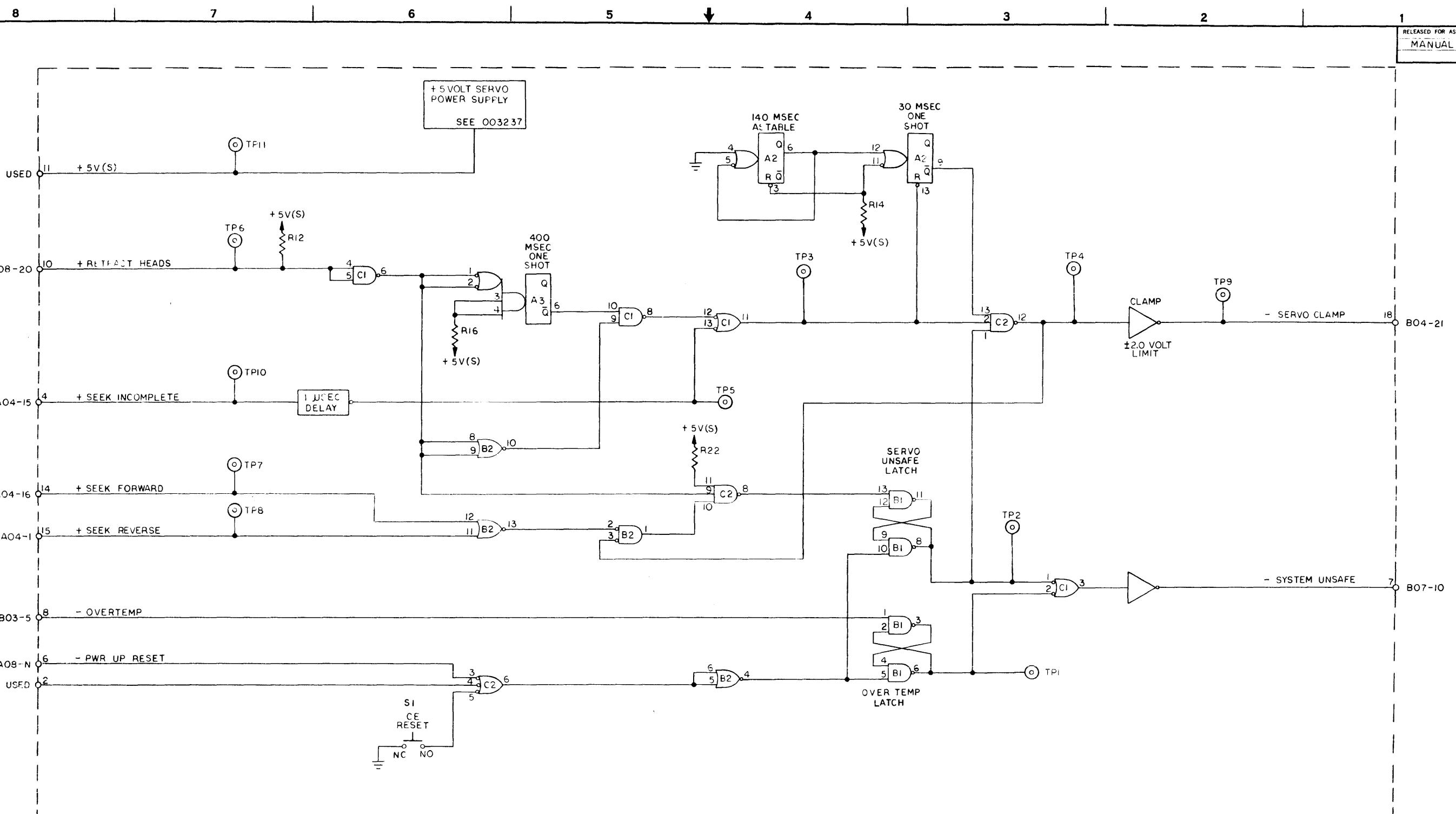










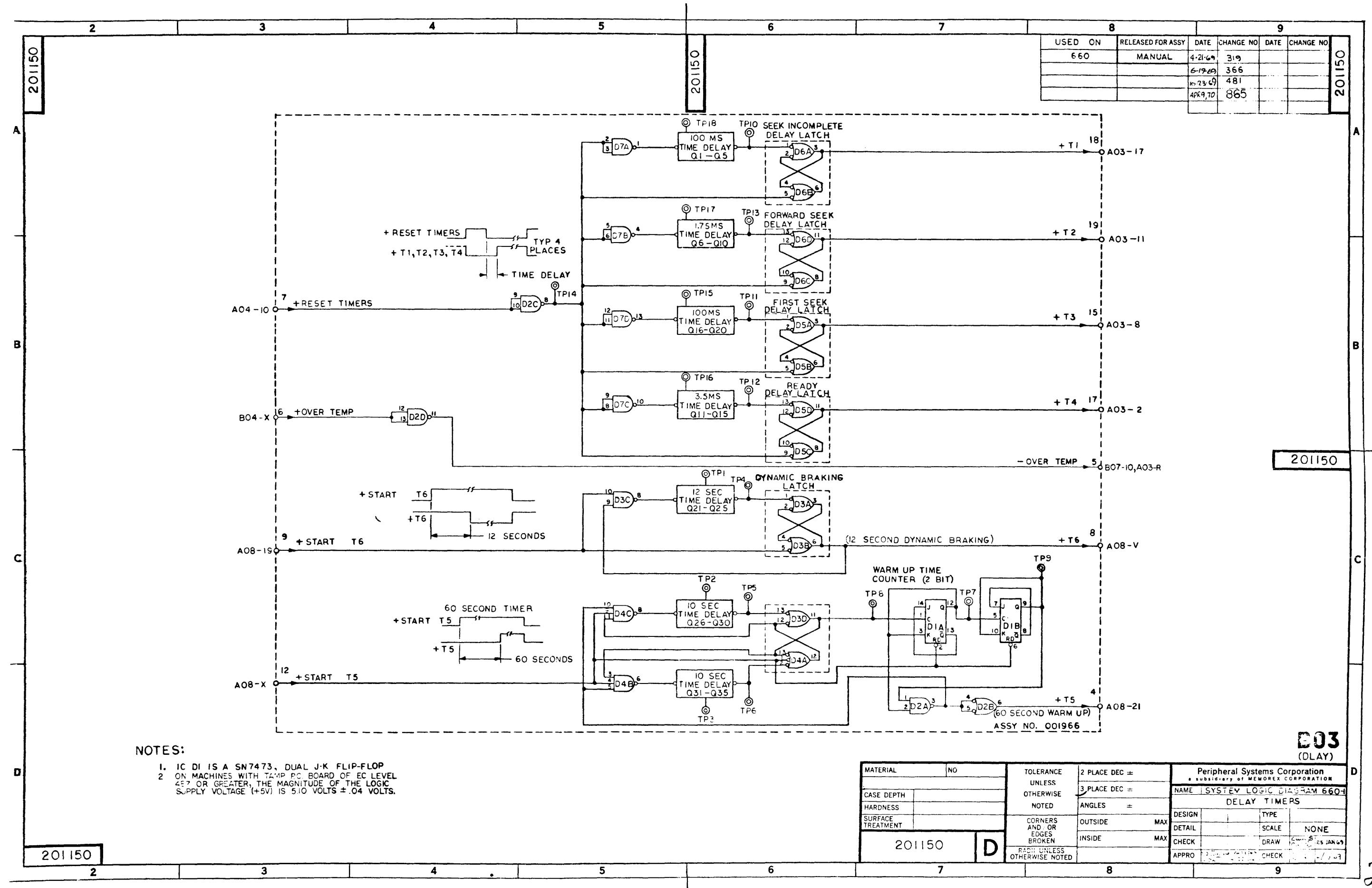


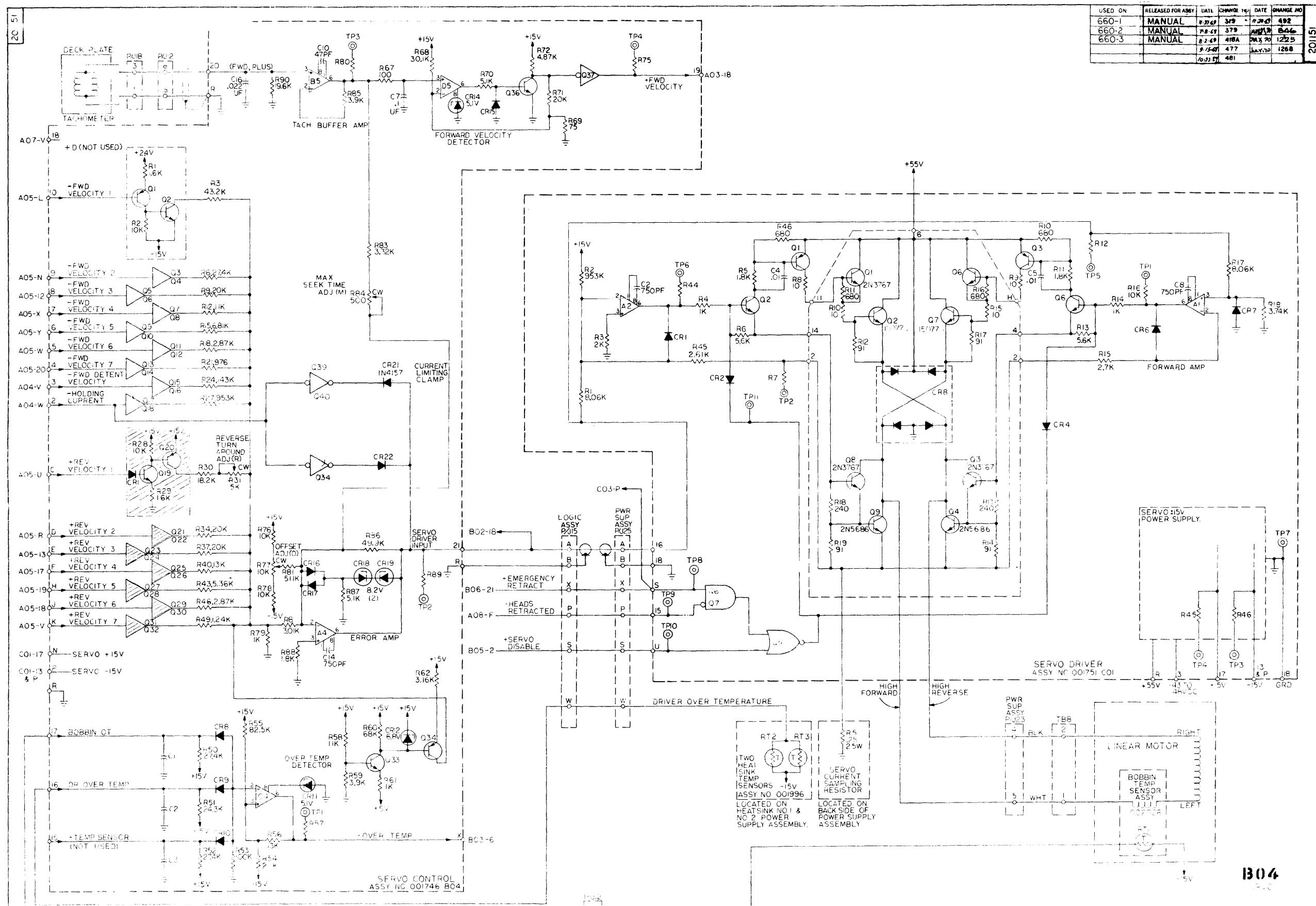
BO 2

DATE	CHANGE NO	DATE	CHANGE NO	TECHNICAL APPROVAL	MEMOREX EQUIPMENT GROUP
JULY 170	1268				NAME SYSTEM LOGIC DIAGRAM SYSTEM PROTECTION
DESIGN					EG
DETAIL CHECK					JUL 31, 70
APPRO					EG JUL 31, 70

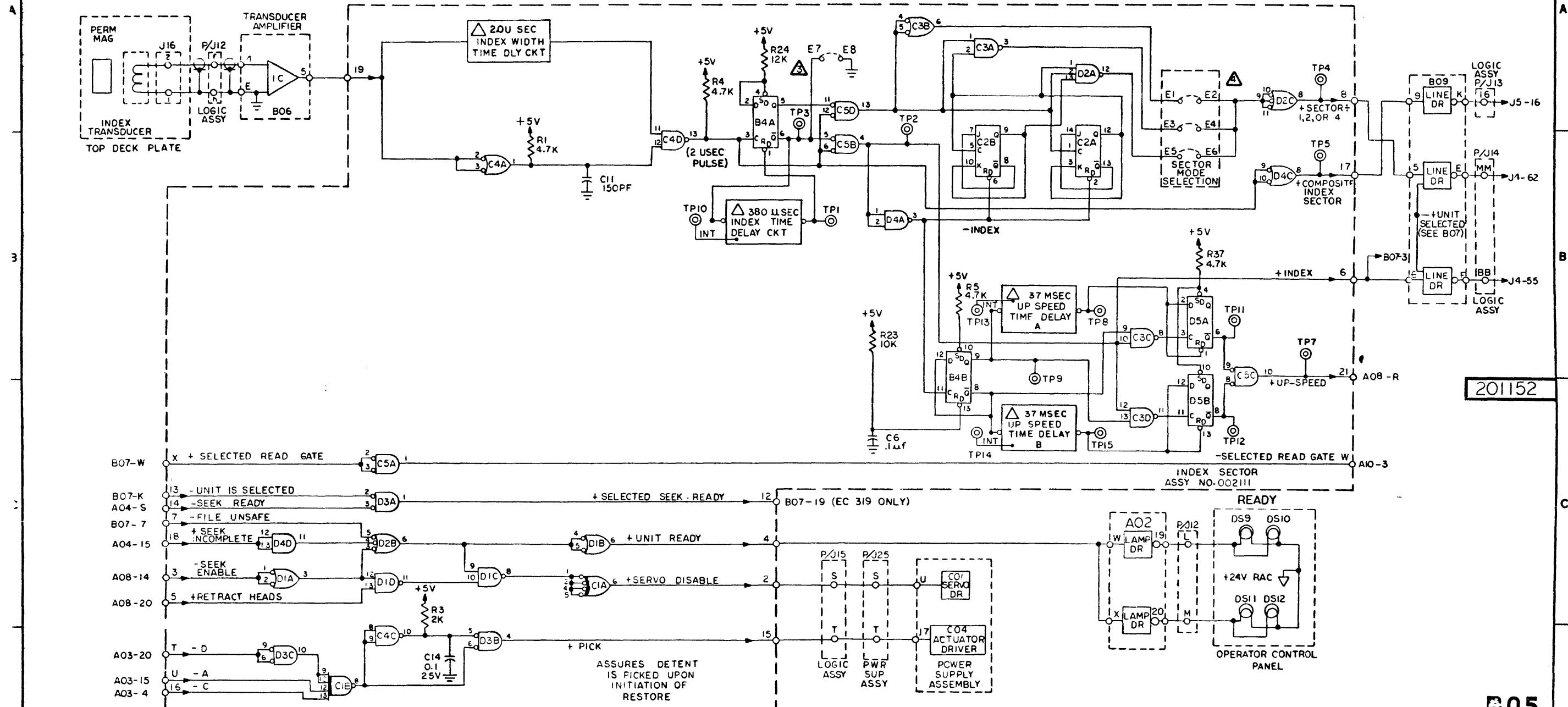
201776

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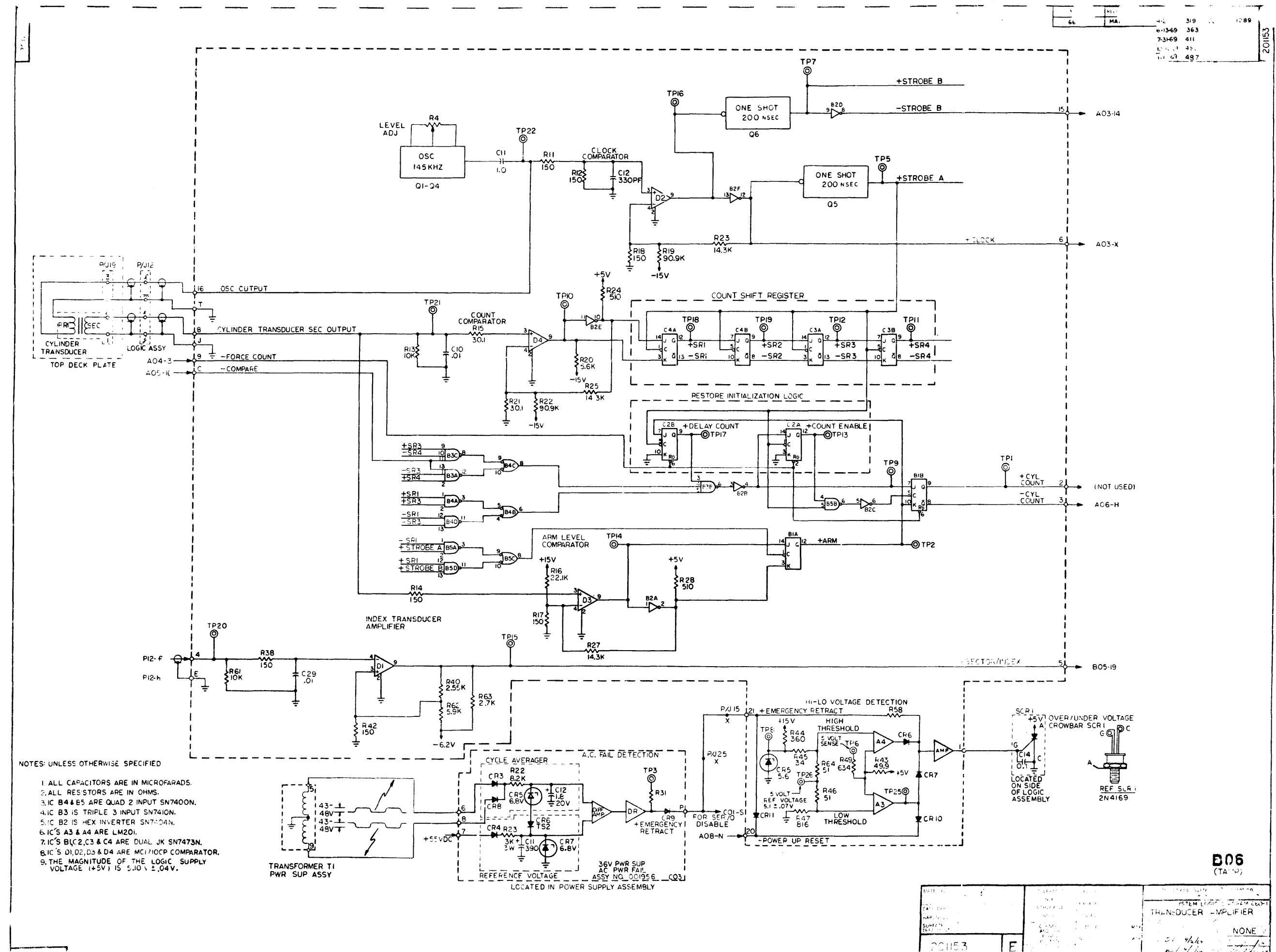


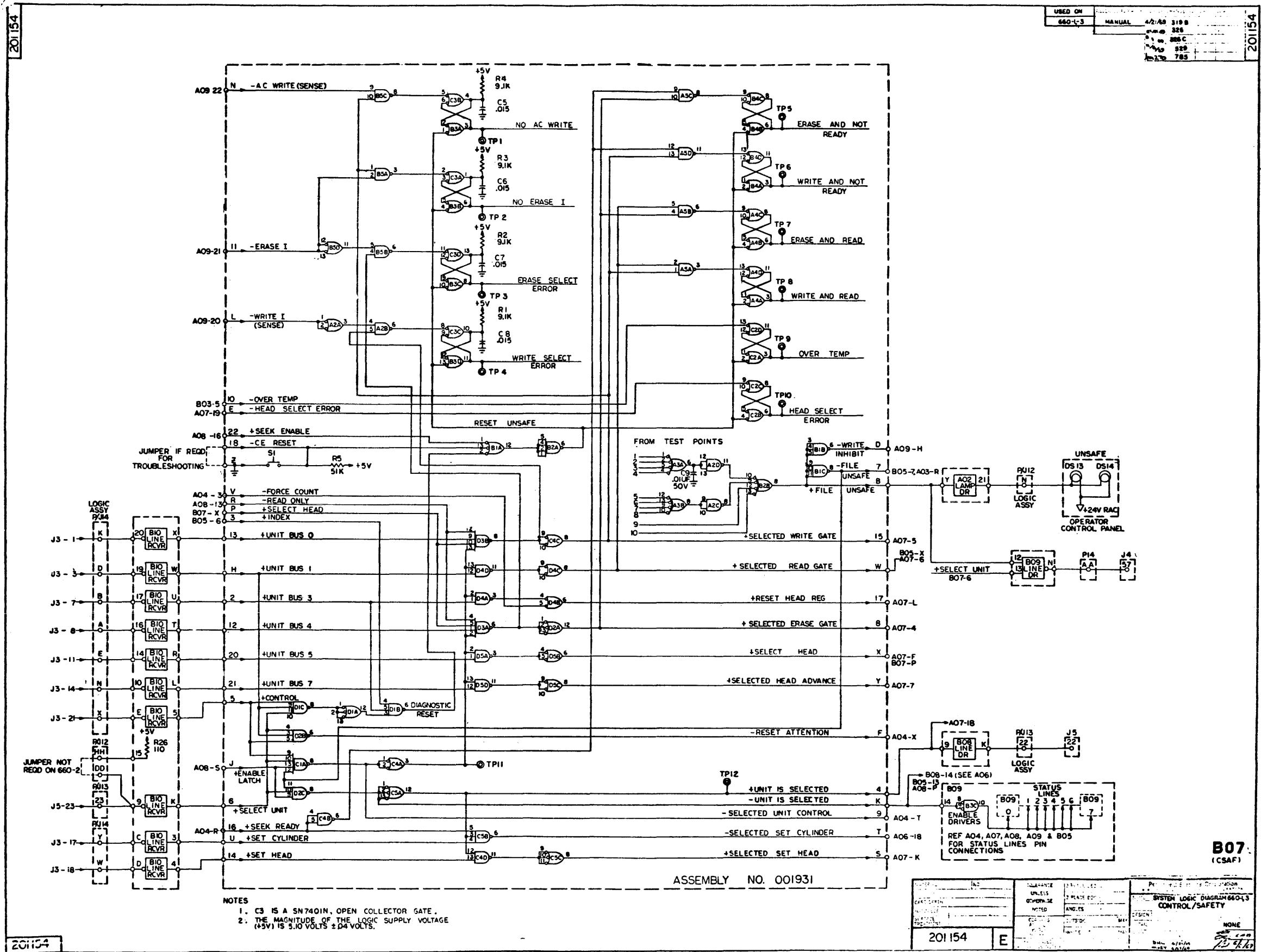
USED ON	RELEASED FOR ASSY	DATE	CHANGE NO	DATE	CHANGE NO
660-1	MANUAL	4-21-69	319	APR 970	865
660-2		5-11-69	320		
		8-19-69	320C		
		10-15-69	468		
		11-14-69	468A		



MATERIAL		NO	TOLERANCE	2 PLACE DEC =	Peripheral Systems Corporation a subsidiary of MEMOREX CORPORATION		
CASE DEPTH			UNLESS OTHERWISE NOTED	3 PLACE DEC =	NAME SYSTEM LOGIC DIAGRAM 660-1		
HARDNESS					INDEX / SECTOR		
SURFACE TREATMENT							
CORNERS AND/OR EDGES BROKEN							
OUTSIDE		MAX					
DETAIL							
INSIDE		MAX					
CHECK							
APPRO							
DRAW							
CHECK							
201152		D					

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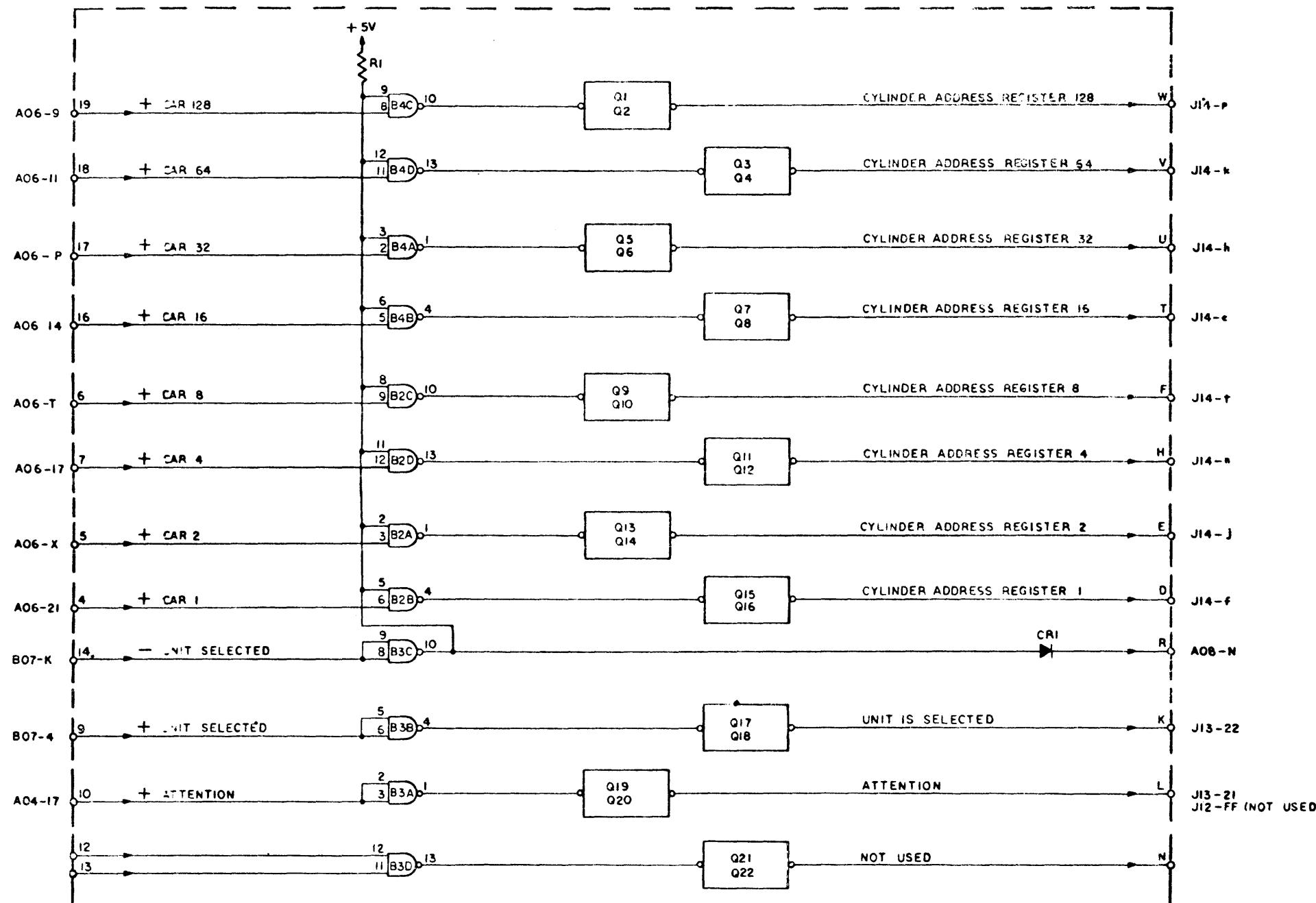




8 7 6 5 4 3 2 1

201328

RELEASED FOR ASSEMBLY
MANUAL



NOTES: UNLESS OTHERWISE SPECIFIED

1. CAR 1 THRU CAR 128 NOT WIRED ON MODEL 660-2.
2. ON MACHINES WITH TAMP P.C. BOARD EC LEVEL 487 OR GREATER, THE MAGNITUDE OF THE LOGIC SUPPLY VOLTAGE (+5V) IS 5.10 VOLTS $\pm .04$ VOLTS.

1308

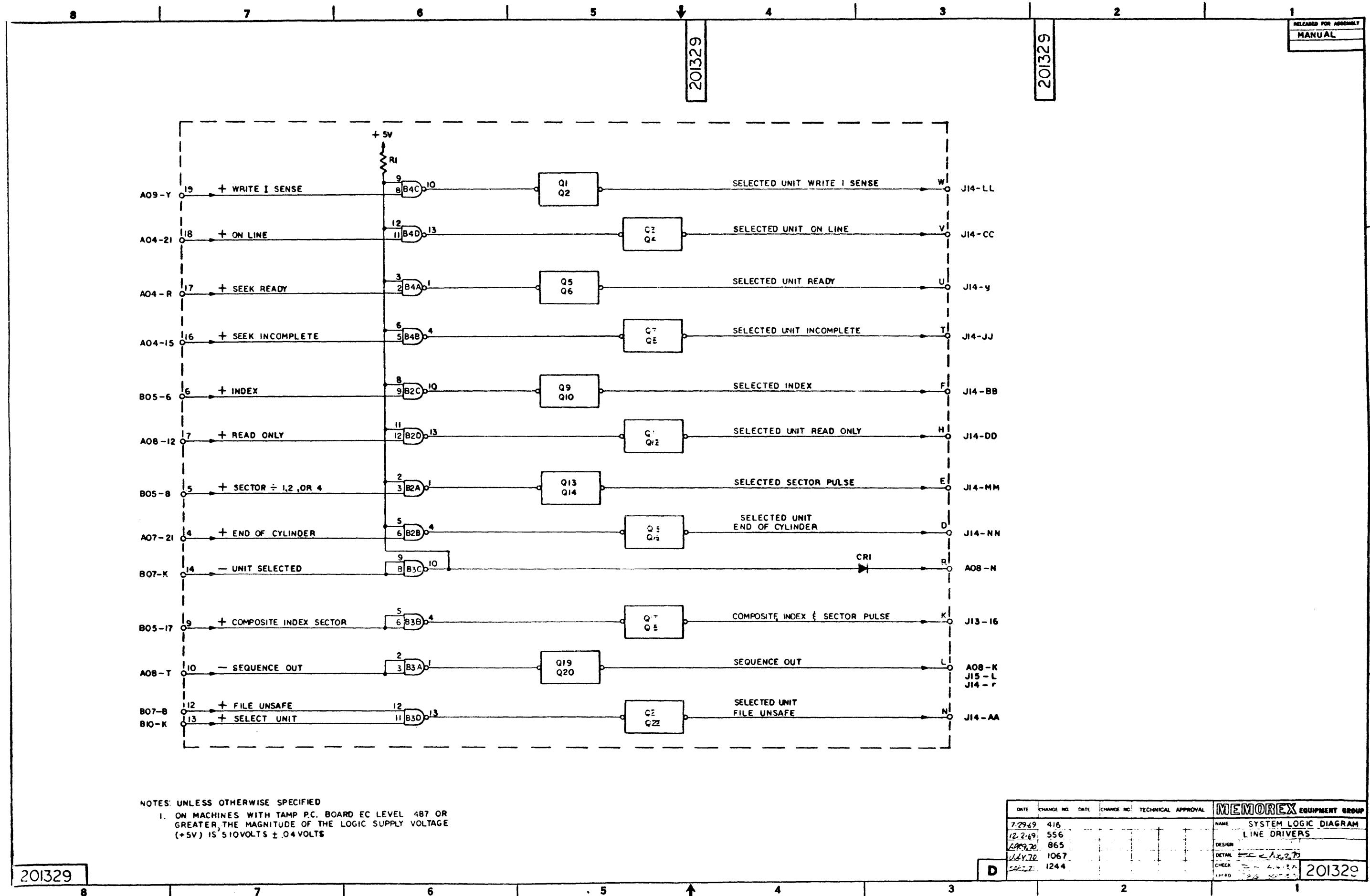
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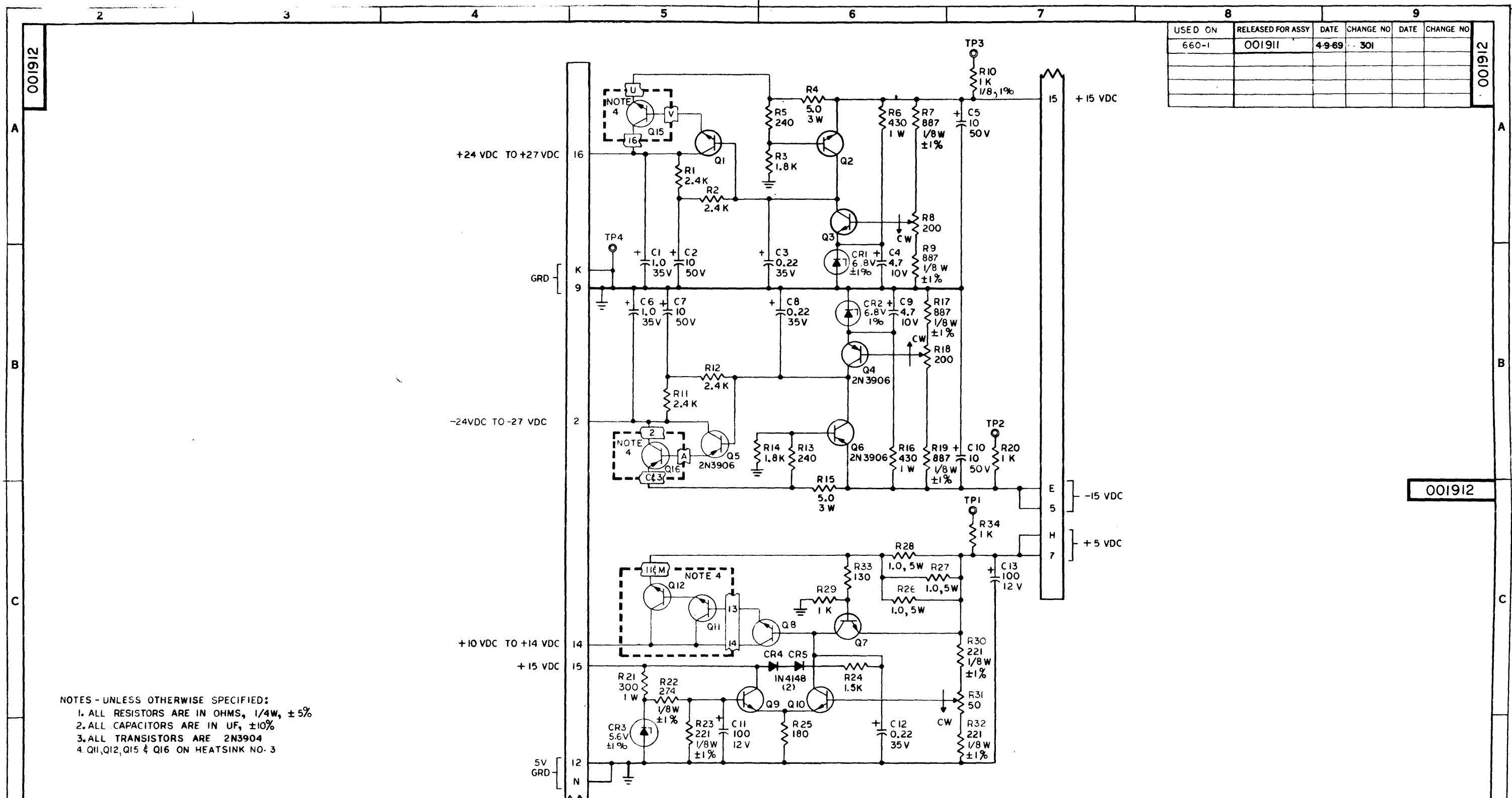
DATE	CHANGE NO.	DATE	CHANGE NO.	TECHNICAL APPROVAL	MEMOREX EQUIPMENT GROUP
7-29-69	416				SYSTEM LOGIC DIAGRAM
12-26-69	556				LIN. DRIVERS
1-23-70	665				
JULY 70	1067				
SEP 70	1244				
					DESIGN
					DETAIL
					CHECK
					APPROV.

201328

201328

25





NOTES - UNLESS OTHERWISE SPECIFIED:

1. ALL RESISTORS ARE IN OHMS, 1/4W, $\pm 5\%$
2. ALL CAPACITORS ARE IN UF, $\pm 10\%$
3. ALL TRANSISTORS ARE 2N3904
4. Q11, Q12, Q15 & Q16 ON HEATSINK NO. 3

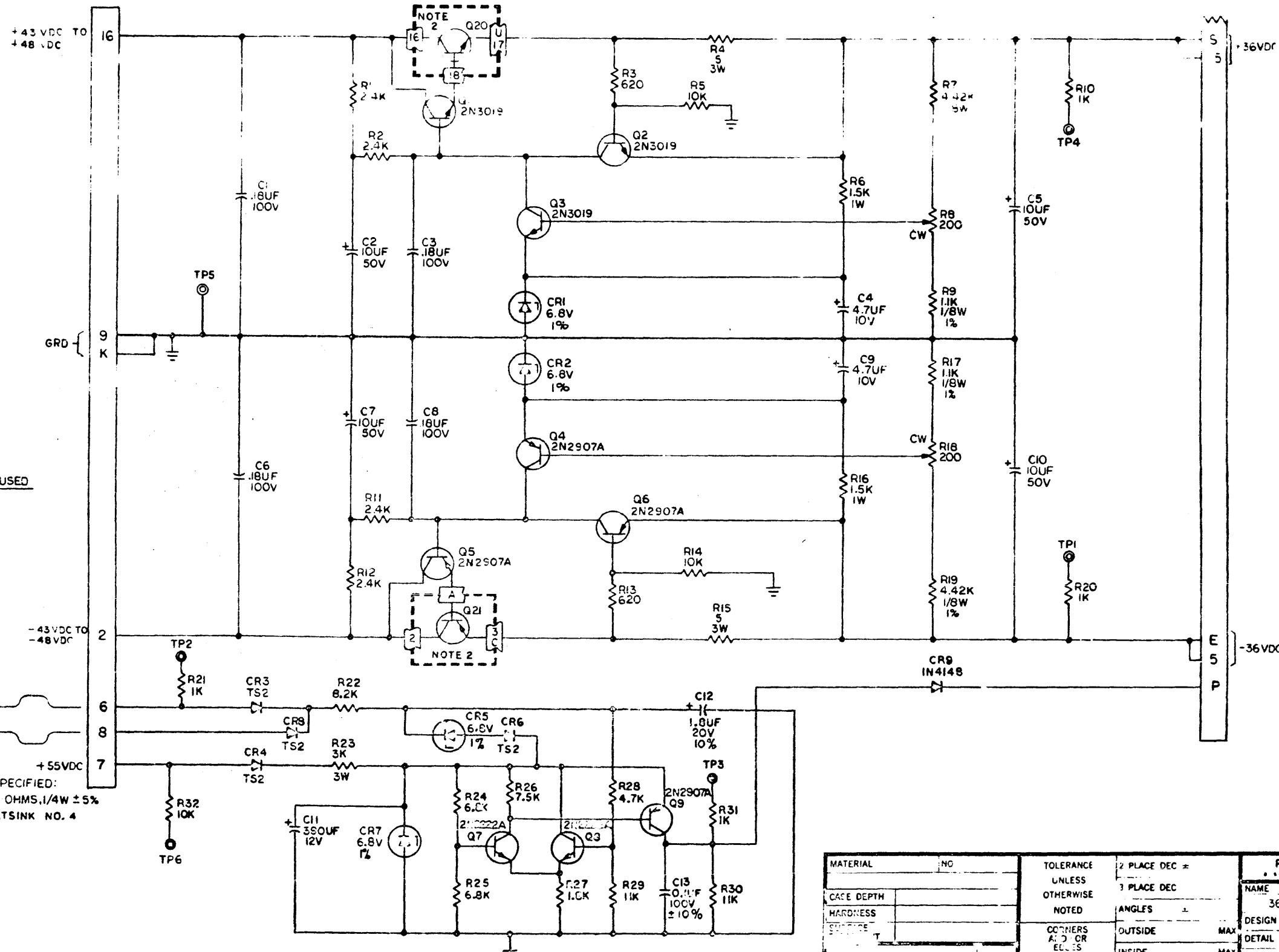
LAST REFERENCE | REFERENCE
DESIGNATION USED | DESIGNATION NOT USED

C13
CR5
Q12
R34
TEA

MATERIAL		NO	TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ±	Peripheral Systems Corporation a subsidiary of MEMOREX CORPORATION		
CASE DEPTH				3 PLACE DEC ±			
HARDNESS				ANGLES ±			
SURFACE TREATMENT				CORNERS AND / OR EDGES BROKEN	OUTSIDE MAX	SCHEMATIC DIAGRAM ±15V & 5V POWER SUPPLY	
001912		D	INSIDE MAX	DESIGN	TYPE		
			RADIUS UNLESS OTHERWISE NOTED	DETAIL	SCALE	NONE	
				CHECK	DRAW	3-14-69	
				APPRO	CHECK	3-14-69	

XC020

2	3	4	5	6	7	8	9
001957			001957			001956	49-69 301 *
						AUG 70 1266	
						OCT 70 1527	



MATERIAL	NO.	TOLERANCE	2 PLACE DEC ±	Peripheral Systems Corporation
UNLESS OTHERWISE NOTED		NOTED	3 PLACE DEC	6 SUBSIDIARY OF MEMOREX CORPORATION
CAGE DEPTH		ANGLES	±	36V PWR SURAC POWER FAIL
HARDNESS		OUTSIDE	MAX	NAME: J. SCHUMACHER
SURFACE T		INSIDE	MAX	DESIGN
				TYPE
				SCALE
				NOTE
				DRAW
				CHECK
				APPRO

001957

XCO30

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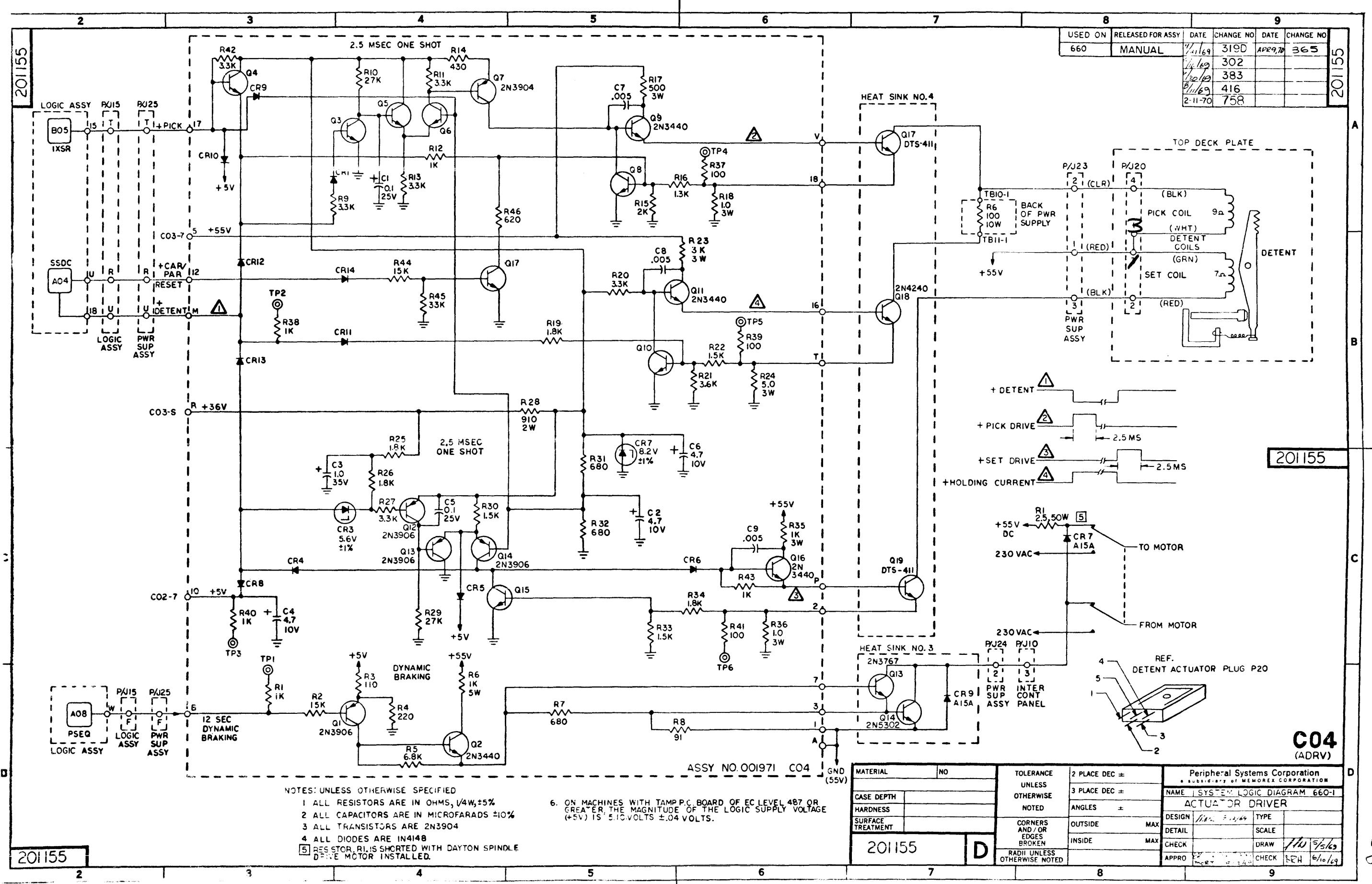
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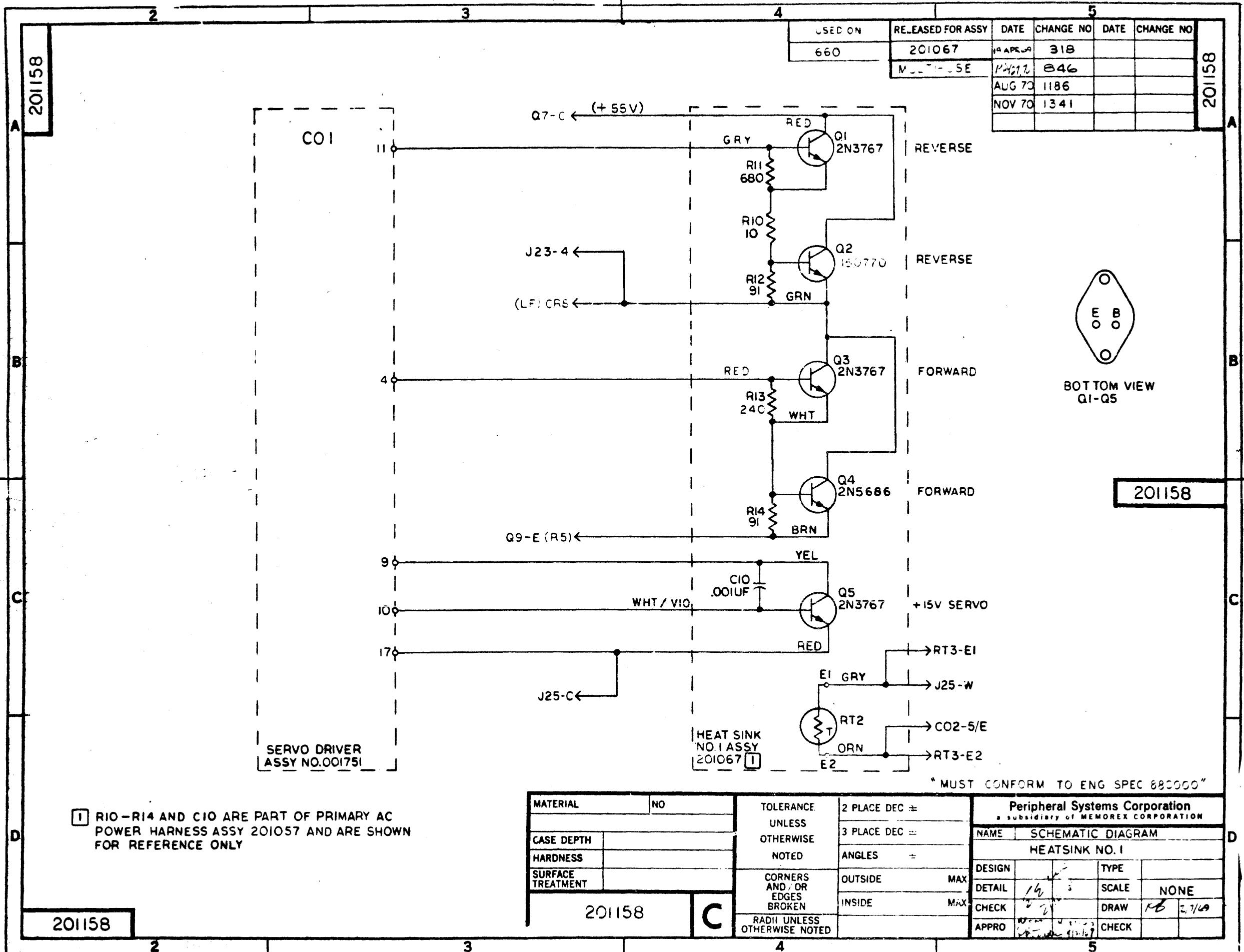
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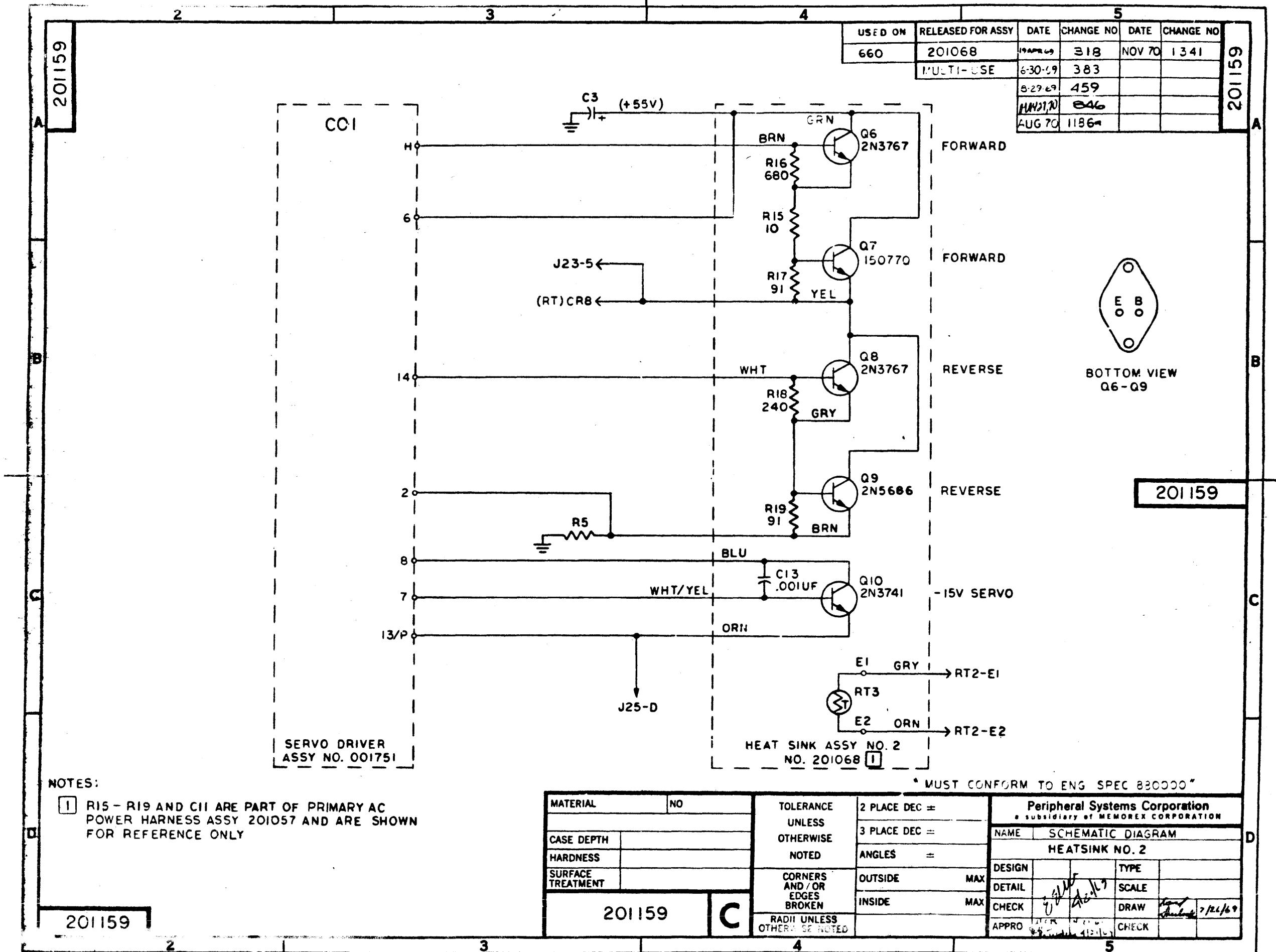
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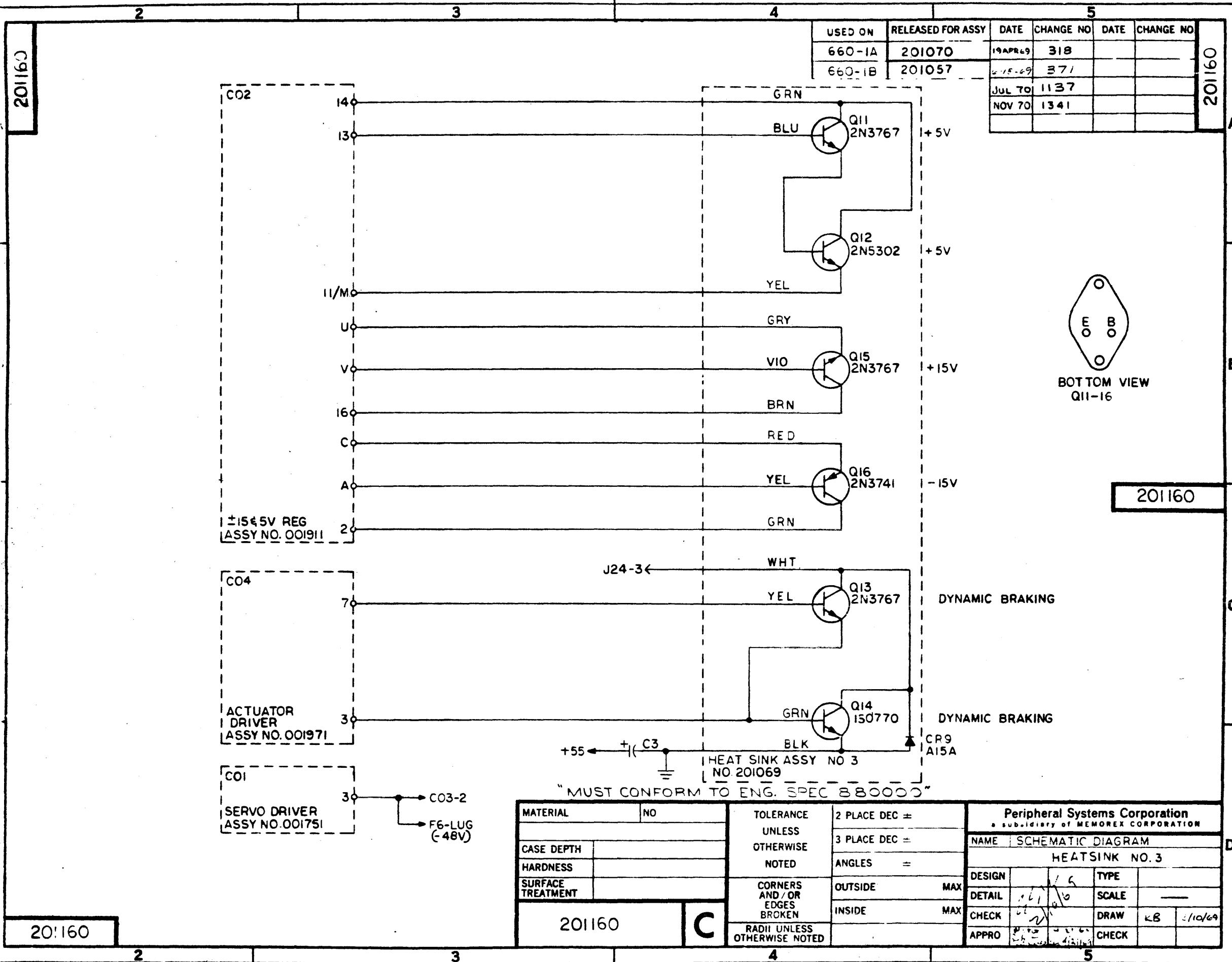
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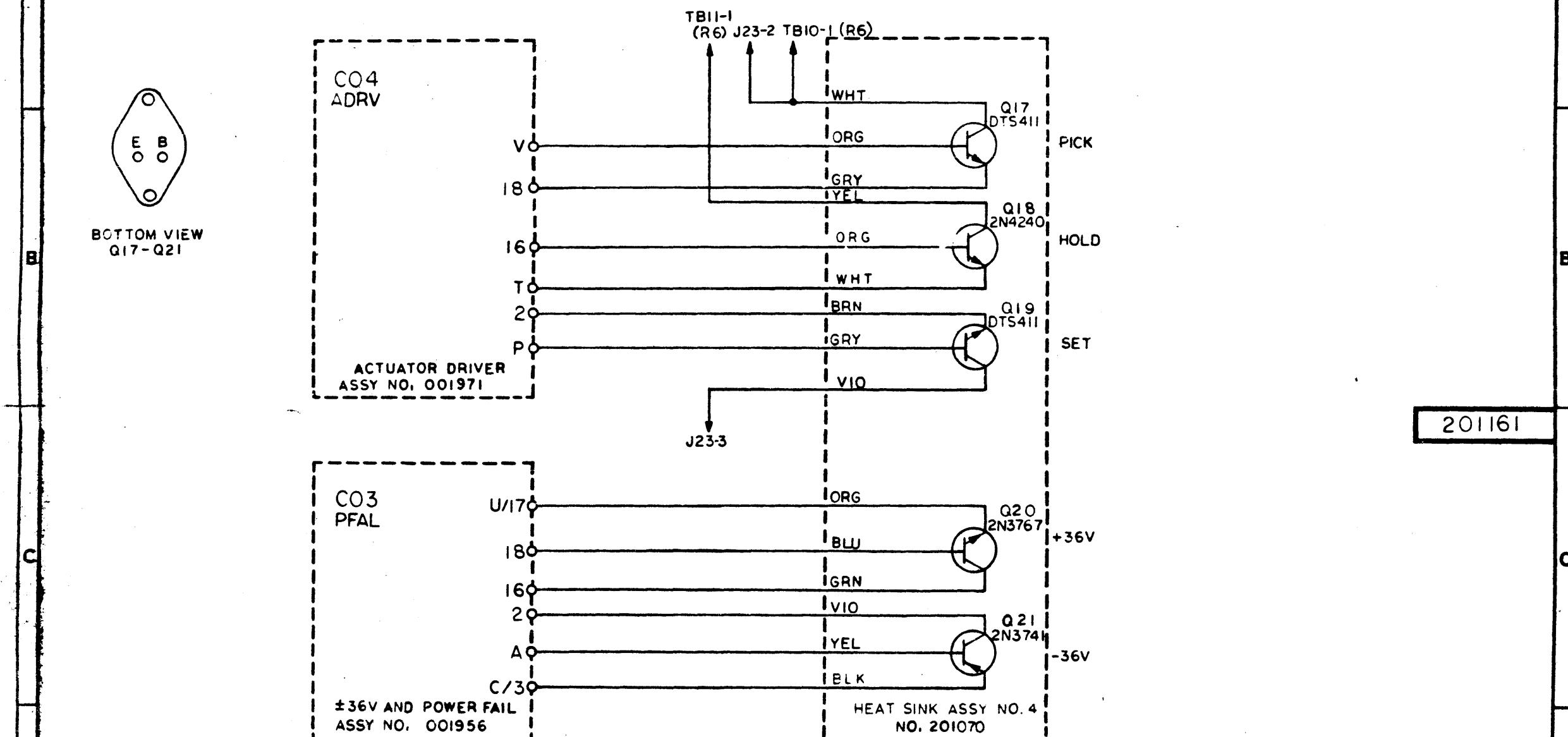






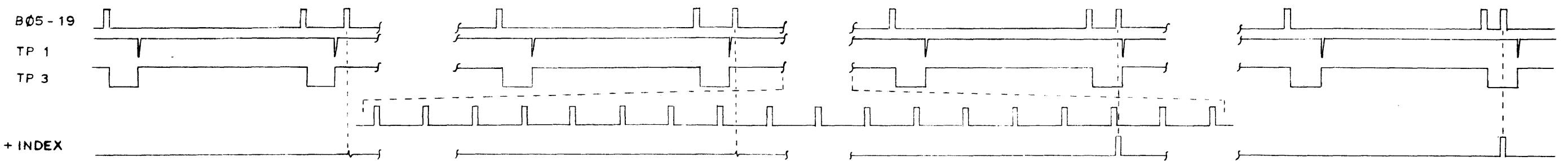


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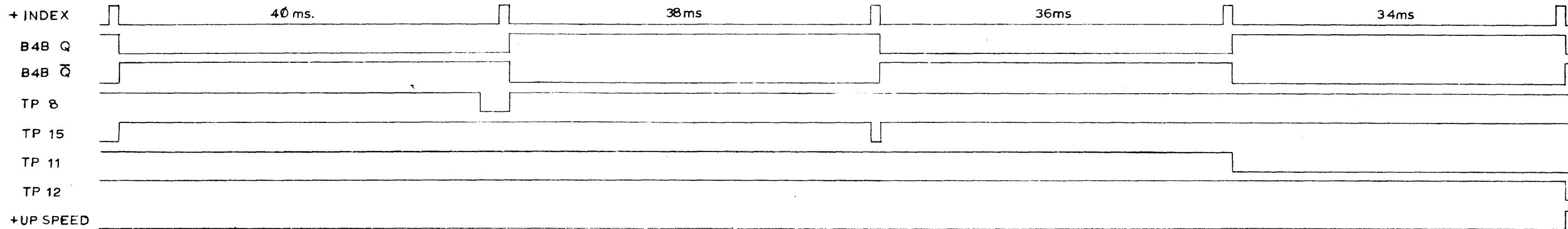


MATERIAL	NO	TOLERANCE UNLESS OTHERWISE NOTED	2 PLACE DEC ±	Peripheral Systems Corporation A Subsidiary of MEMOREX CORPORATION		
CASE DEPTH			3 PLACE DEC ±			
HARDNESS			ANGLES ±			
SURFACE TREATMENT			CORNERS AND/OR EDGES BROKEN	OUTSIDE MAX	HEAT SINK NO. 4	
201161		RADIUS UNLESS OTHERWISE NOTED	INSIDE MAX	DESIGN	TYPE	
C				DETAIL	SCALE	None
				CHECK	DRAW	Memorex Serial No. - FEB 1960
				APPRO	CHECK	

INDEX AND SECTOR



UP TO SPEED TIMING



CYLINDER TRANSDUCER COUNT TIMING

